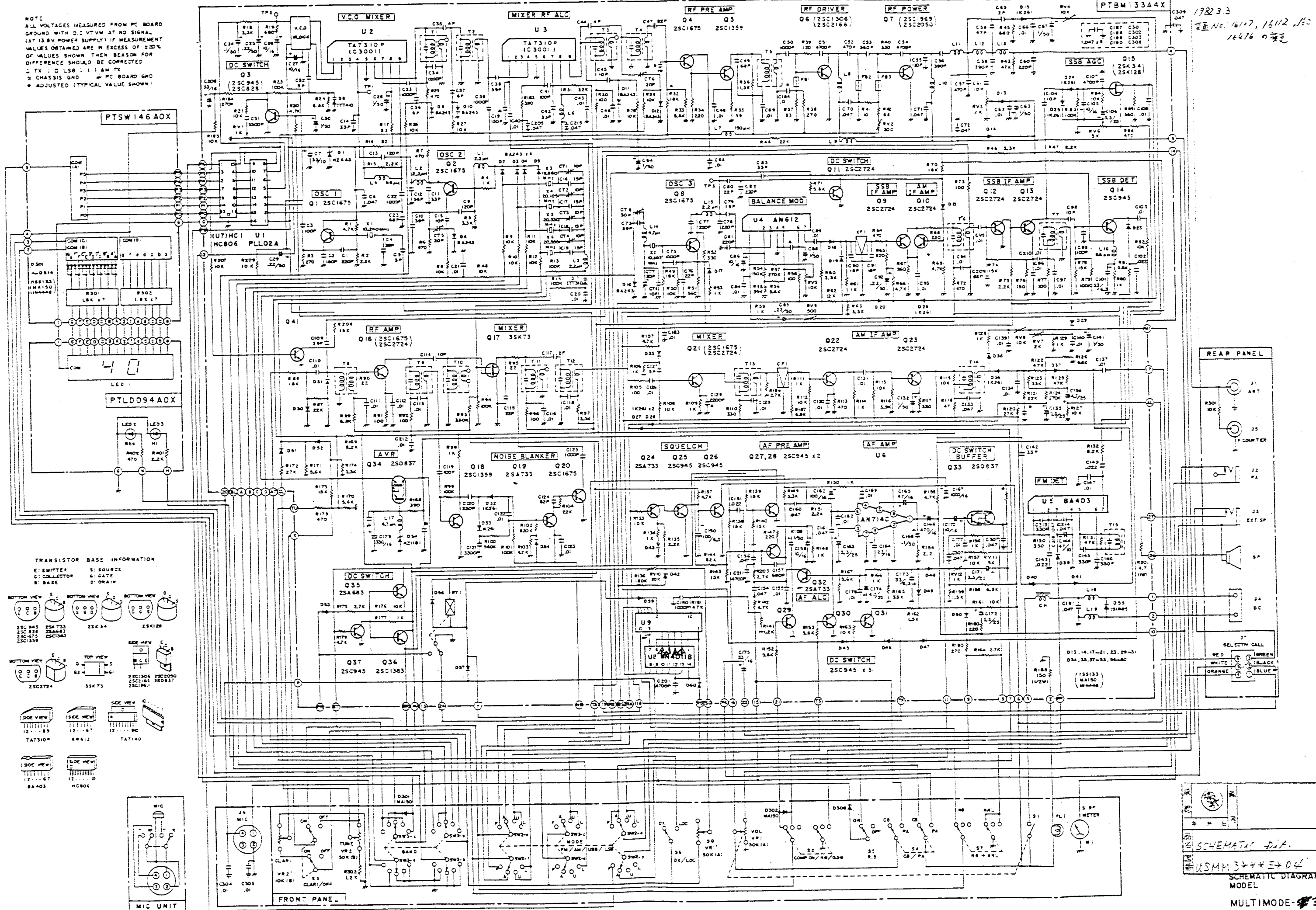


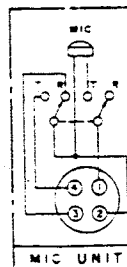
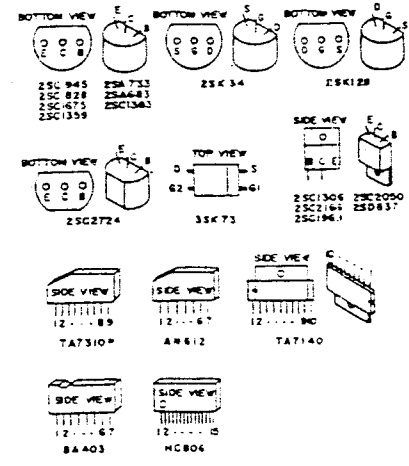
NOTE
 ALL VOLTAGES MEASURED FROM PC BOARD
 GROUND WITH D.C. VTM AT NO SIGNAL
 (AT 13.8V POWER SUPPLY) IF MEASUREMENT
 VALUES OBTAINED ARE IN EXCESS OF 20%
 OF VALUES SHOWN THEN REASON FOR
 DIFFERENCE SHOULD BE CORRECTED
 TX : □ LSB : □ AM TX
 □ CHASSIS GND □ PC BOARD GND
 * ADJUSTED (TYPICAL VALUE SHOWN)

1982.3.3
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 16476 の 電 号



TRANSISTOR BASE INFORMATION

E: EMITTER S: SOURCE
 C: COLLECTOR G: GATE
 B: BASE D: DRAIN



SCHEMATIC DIAGRAM
 MODEL
 MULTIMODE-3
 1981-9-10 1982.3.3