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I put a lot of time into producing these files which is why you are met with this page when you open the file.

In order to generate this file, I need to scan the pages, split the double pages and remove any edge marks such as punch holes, clean up the pages, set the relevant pages to be all the same size and alignment. I then run Omnipage (OCR) to generate the searchable text and then generate the pdf file.

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It is my hope that you find the file of use to you personally – I know that I would have liked to have found some of these files years ago – they would have saved me a lot of time !

Colin Hinson

In the village of Blunham, Bedfordshire.

These are the diagrams that belonged to Mr K J Fison who worked in 2T block and contain his own notes on the diagrams.

TRAINEE NOTE

BRITISH TACAN PART II DIAGRAMS

This training note is issued for the guidance of trainees during training at R.A.F. Locking. No amendments will be issued in respect of modifications introduced to the equipment referred to in this note

This note is not intended as a substitute for the relevant Air Publication and must not be regarded as authority for modifications, servicing procedures, etc.

RESTRICTED

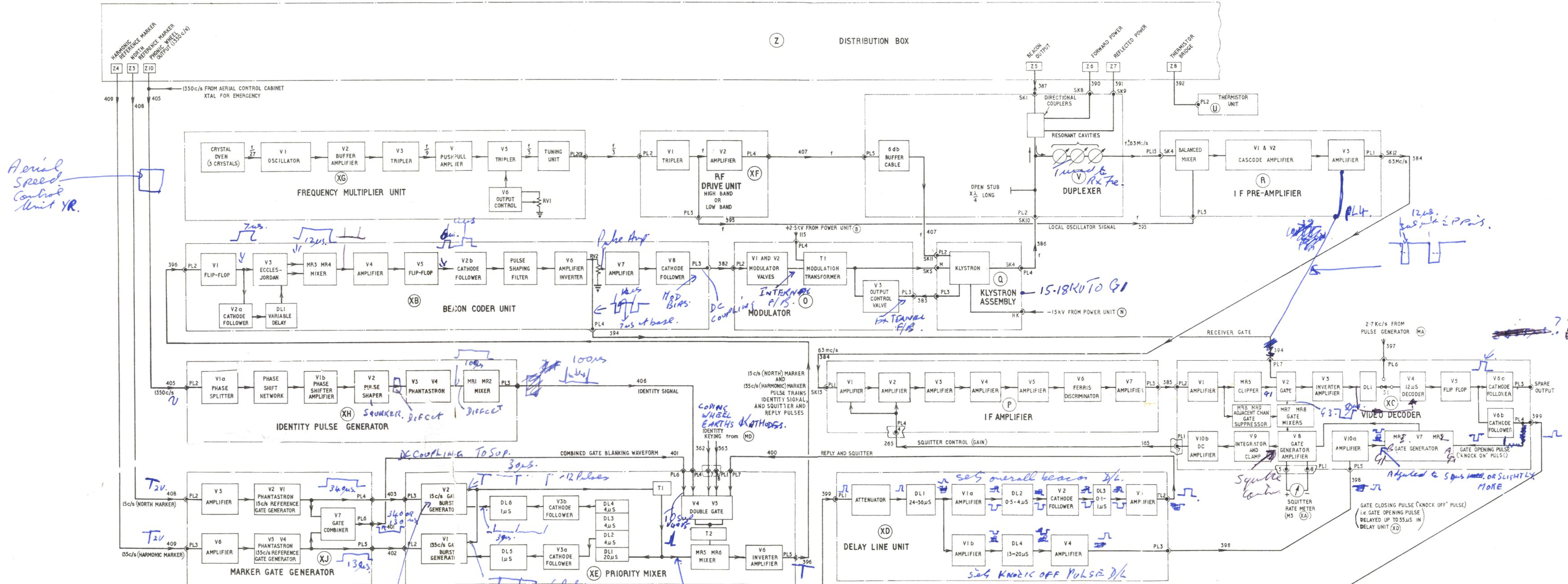
PART II DIAGRAMS

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FIG. NO.	TITLE
✓ 30	Transmitter Receiver Block Diagram (Signal)
✓ 31	Aerial Control Cabinate - Primary Power Supply
✓ 32 ✓	Voltage Regulator (YO) and Control Unit (YN)
✓ 33 ✓	Aerial Speed Control Unit (YR) <i>D969</i>
34	Aerial Speed Rectifier Unit (YS)
35	Meter Panel (YJ)
36	+50 V Power Unit (C)
37	50 V Power Supply Warning and Interlock Circuit
38	Control Units (W and RC) <i>D962</i>
39	Distribution Box (Z)
40	Transmitter Receiver Primary Power Supply <i>961</i>
41	+500 V Power Unit (A)
42	+2.5 kV Power Unit (B)
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47	I.F. Amplifier (P)
48 ✓	Video Decoder (XC) <i>D965</i>
49	Delay Line Unit (XD)
50 ✓	Priority Mixer (XE)
51	Marker Gate Generator (XJ)
52	Identity Pulse Generator (XH)
53	Identity Keyer (MD)
54 ✓	Beacon Coder Unit (XB) <i>D956</i>
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63	I.F. Signal Generator (MC) <i>D970</i>
64	Monitoring Unit (L) <i>D963.</i>
65	Filter Unit (ME)
66	Co-axial Interconnections
67	R.H. Cabinate Interconnections
68	L.H. Cabinate Interconnections

Period No a	Period Ref b	Lab No c	Prep Ref d	e	Subject f
					FGRI 18119 <u>53 Theory Periods</u>
97-99				L	Principles of operation in Tacan with air/ground system diagram. Introduction to system with concise details.
100		9		P	Demonstration of equipment in operation with emphasis on safety precautions.
101-104				L	Circuit details and action of control sequence diagram units W, RC and Z.
105-111				L	Circuit details and action of all power units and distribution.
112		9		P	Security and functional checks on the equipment.
113-115		9		P	Class practice in switching on and running up.
116		9		P	Fault recognition and location, class practice. Inspection of air blower BG and air pressure tilt switch.
117-118				L	Circuit diagram details and action of voltage regulator units YO and YN.
119-121		9		P	Fault recognition and location, class practice.
122-125				L	Circuit details and action of aerial speed control system and phonic wheel units YR and YS.
126		9		P	Routine servicing demonstration of aerial speed control setting up.
127-128		9		P	Routine servicing class practice.
129-131		9		P	Fault recognition and location, class practice.
132				L	Introduction to test equipment used and its use.
133-137				L	Circuit details and action of pulse generator, RF signal generator and thermistor unit. MA
138-141				L	Circuit details and action of IF signal generator and monitor unit L.
142-145				L	Circuit details and action of RF carrier chains XF and XG.
46-147		9		P	Routine servicing, setting up tuning and checking.
48-151				L	Circuit details and action of reference units XJ and XE .
152		9		P	Class inspection of aerial layout showing pick up coils etc.
53-154		9		P	Routine servicing setting up. North reference and harmonic reference with test equipment.
55-156		9		P	Routine servicing, setting up reference pulses.
57-158		9		P	Fault recognition and location, class practice.

Period No a	Period Ref b	Lab No c	Prep Ref d	e	Subject f
3 159-161				L	Circuit details and action of identity chain units XH and MD.
162-163		9		P	Setting up procedure with class practice.
164-165		9		P	Fault recognition and location, class practice.
5 166-170				L	Circuit details and action of squitter chain including units P, XC, XD and R, and duplexer.
171		9		P	Setting up squitter rate and video decoder with test equipment.
172-175		9		P	Fault recognition and class practice.
176-180		9		P	Routine servicing, signal/noise ratio check class practice.
1 181				L	Revise all chains leading to priority determination block.
4 182-185				L	Circuit details and action of reply and information chain units XB, O and Q.
186		9		P	Routine servicing modulator setting up procedure.
187-189		9		P	Routine servicing, class practice.
190		9		P	Transmitter tuning and output power check. Safety switch check as per monthly.
191		9		P	Demonstration of overall beacon delay.
192-194		9		P	Routine servicing and fault finding, class practice.
195-199		9		P	Demonstration of klystron assembly, removal and replacement.
3 200-202				L	Theory examination AN/URN 3 and FGRI 18119.
203-212		9		P	Final practical examination on AN/URN 3 and FGRI 18119.
					<u>ACR7D</u>
213-214				L	Introduction, purpose of equipment, operating techniques, siting and limitations.
215-216				L	BSD of ACR7D general description and concise details.
217-218				P	Demonstration of equipment and general layout.
219-220				L	BSD of radar head type 8045. Detailed description.
221-222		2		L	Circuit details and action of modulator unit.
223				L	Details of magnetron assembly.
224-225				L	Circuit details and action of power supplies.
226-227		15/1		P	Class inspection of modulator, EHT and magnetron compartments.



AIR DIAGRAM
6264A/MIN.
ISSUE 1 PREPARED BY MINISTRY OF SUPPLY FOR PROMULGATION BY AIR MINISTRY

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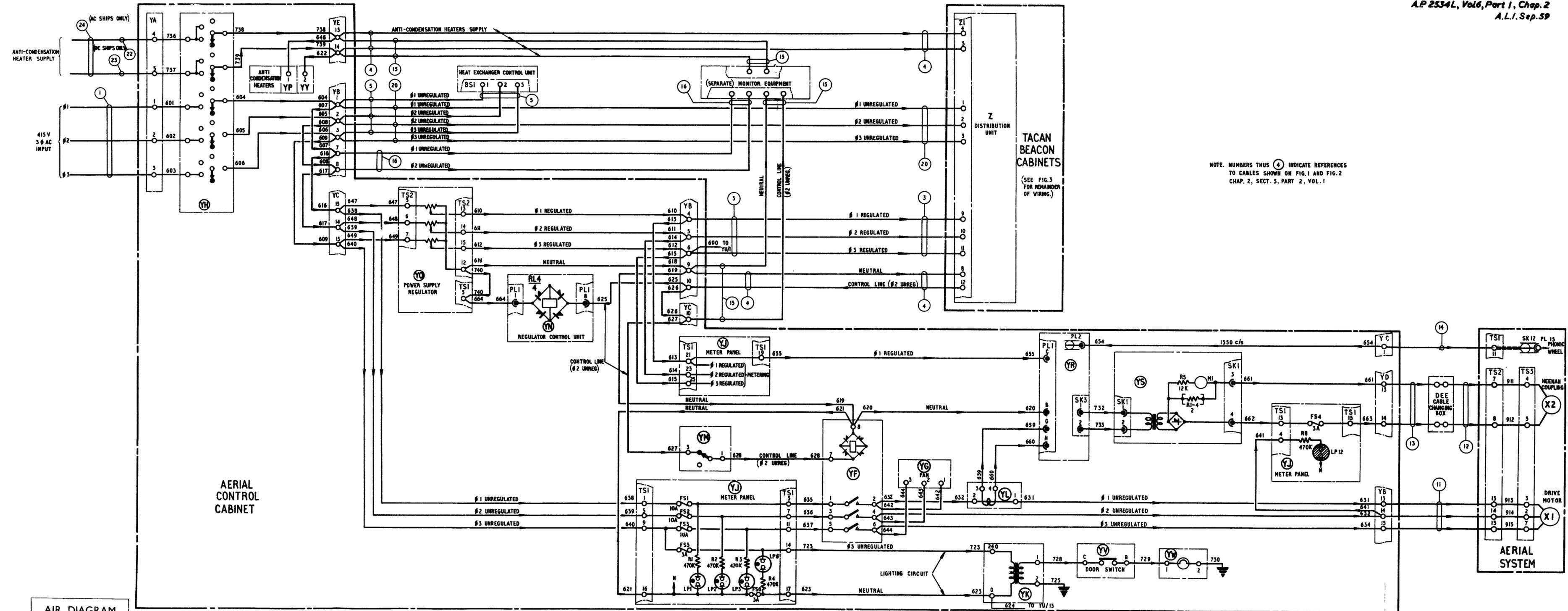
Transmitter-receiver, 5825-99-932-5320 - block diagram (signal)

Fig. 30 (A.L.1, Aug.58)

+ve fed to 93
dia w/f fed to 91
Pulses fed to V2 1.5-3µs wide
DL1-6 Adjusted to produce correct
Pulses EITHER. 2µs OR 300µs NOT BOTH.

300µs
Pulses EITHER. 2µs OR 300µs NOT BOTH.

Adjusted to 5µs. OR SLIGHTLY MORE
GATE CLOSING PULSE (KNOCK OFF PULSE)
i.e. GATE OPENING PULSE DELAYED UP TO 55µs IN DELAY UNIT (XD)

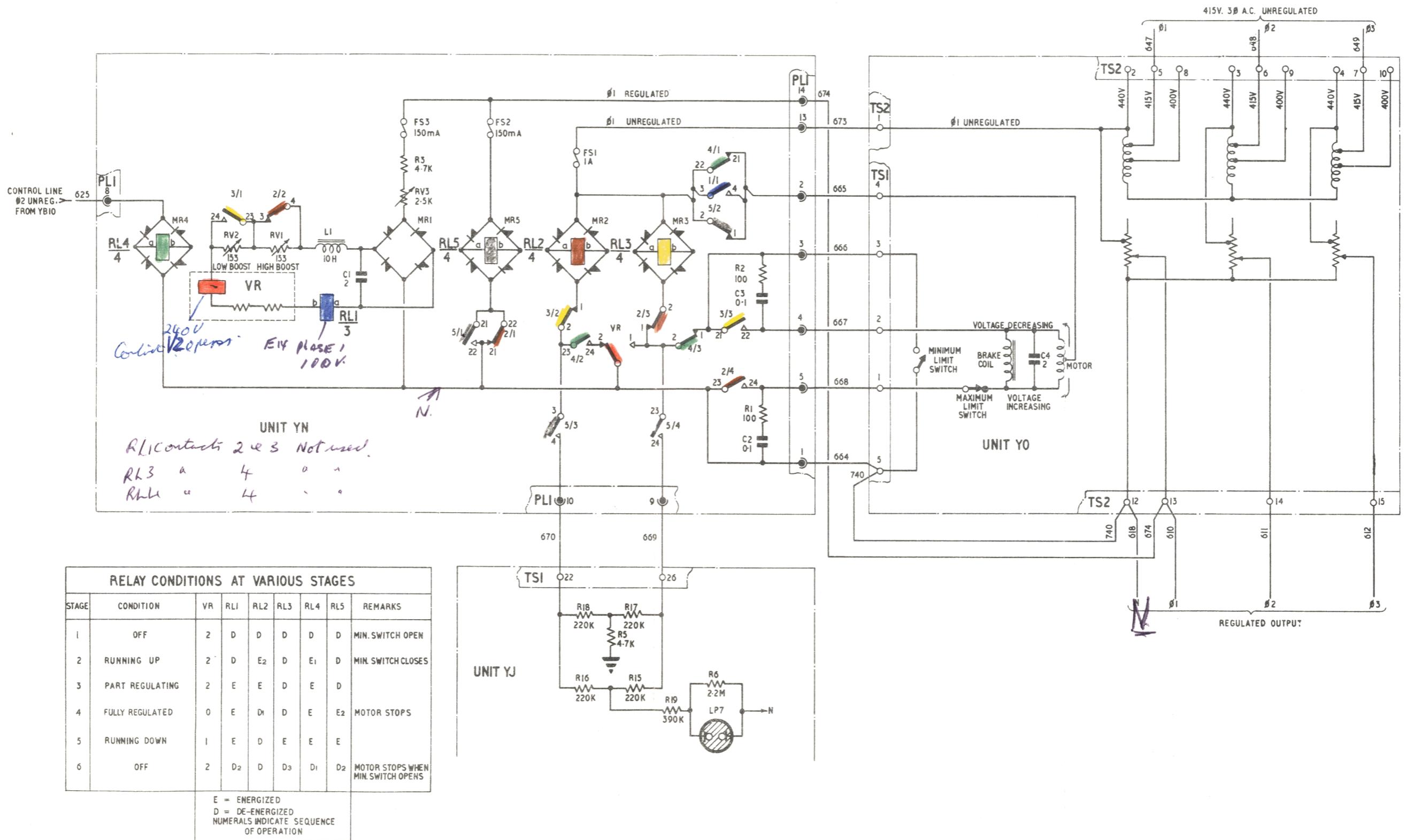


NOTE. NUMBERS THUS (4) INDICATE REFERENCES TO CABLES SHOWN ON FIG. 1 AND FIG. 2 CHAP. 2, SECT. 5, PART 2, VOL. 1

AIR DIAGRAM
6264AQ/MIN.
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SRI. 18118 FGRI. 18119- Aerial control cabinet primary power supply-circuit

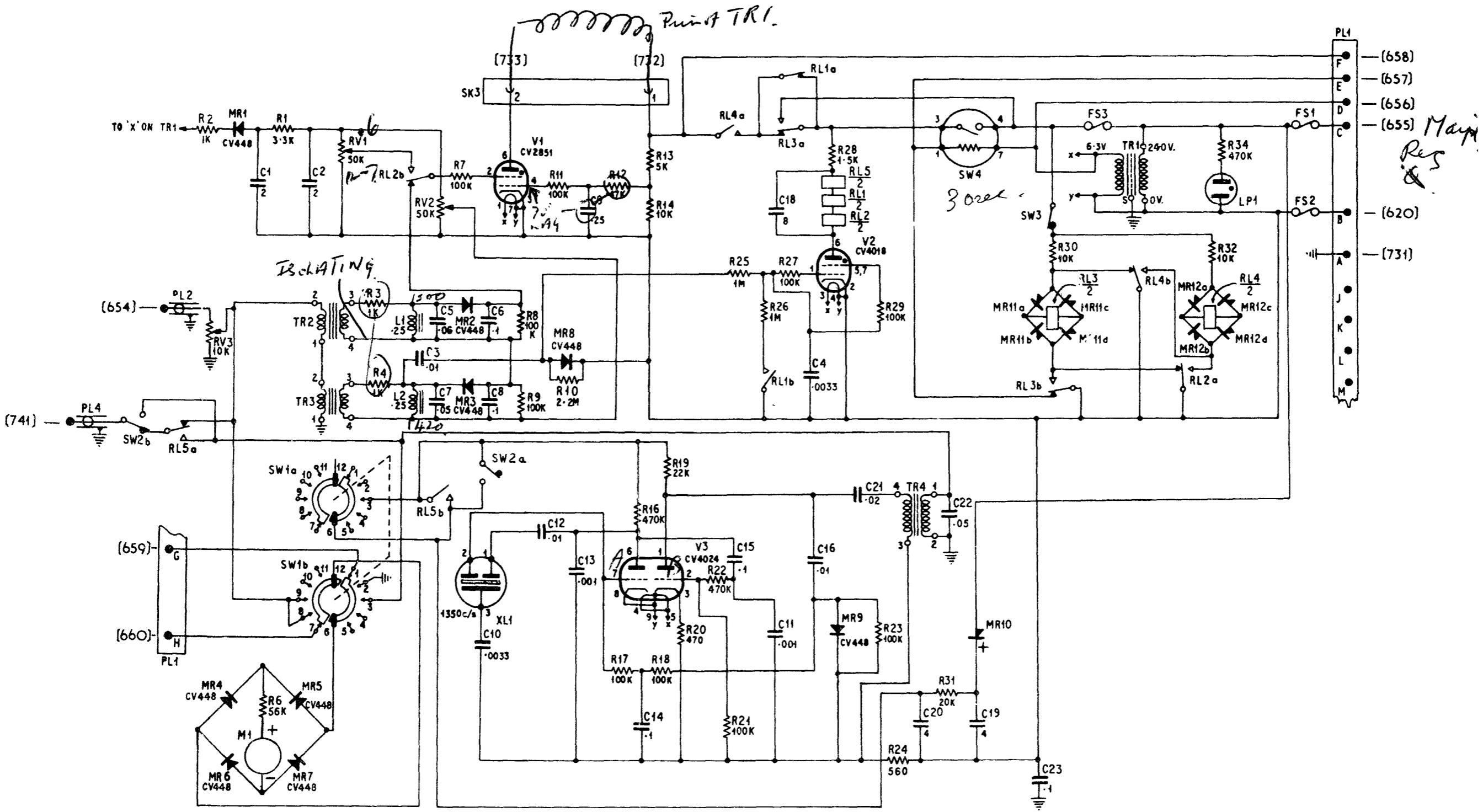
Fig. 31



SRI.18118. FGRI.18119-Voltage regulator (YO) and control unit (YN)- circuit

AIR DIAGRAM
 6264AM/MIN.
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 FOR PROMULGATION BY AIR MINISTRY

RESISTORS	RV3 R2	R1 R6	RV1 R4	R3 R4	R7 RV2	R8 R11 R9 R10	R12 R13 R14 R17 R16 R18 R19 R20 R22 R21	R25 R26 R27 C15 C11 C18 C4 C16 C21	R28 R23	R29 R24 R31	R30	R32
CAPACITORS	C1	C2	C3 C5 C7 C10 C6 C8	C12 C13 C9 C14	C15	C11 C18 C4 C16 C21	C20 C22 C19	C23				
MISCELLANEOUS	PL1 PL2 PL4 SW2b RL5a MR4 MR6 M1 MR5 MR7 SW1a SW1b	MR1 MR4 MR6 M1 MR5 MR7 SW1a SW1b	TR2 TR3	RL2b RL5b MR2 MR3 V1 L1 L2 SW2a XL1	SK3 MR8 V3	RL4a RL3a RL1a V2 RL5 RL1 RL1b MR9 RL2 TR4	SW4 MR10 MR1a MR1b MR1c MR1d	SW3 MR1a MR1b MR1c MR1d	RL3b RL3 FS3 TR1 RL4b RL2a MR12a MR12b MR12c MR12d FS2	LP1 RL4 LP1	FS1 PL1	FS2

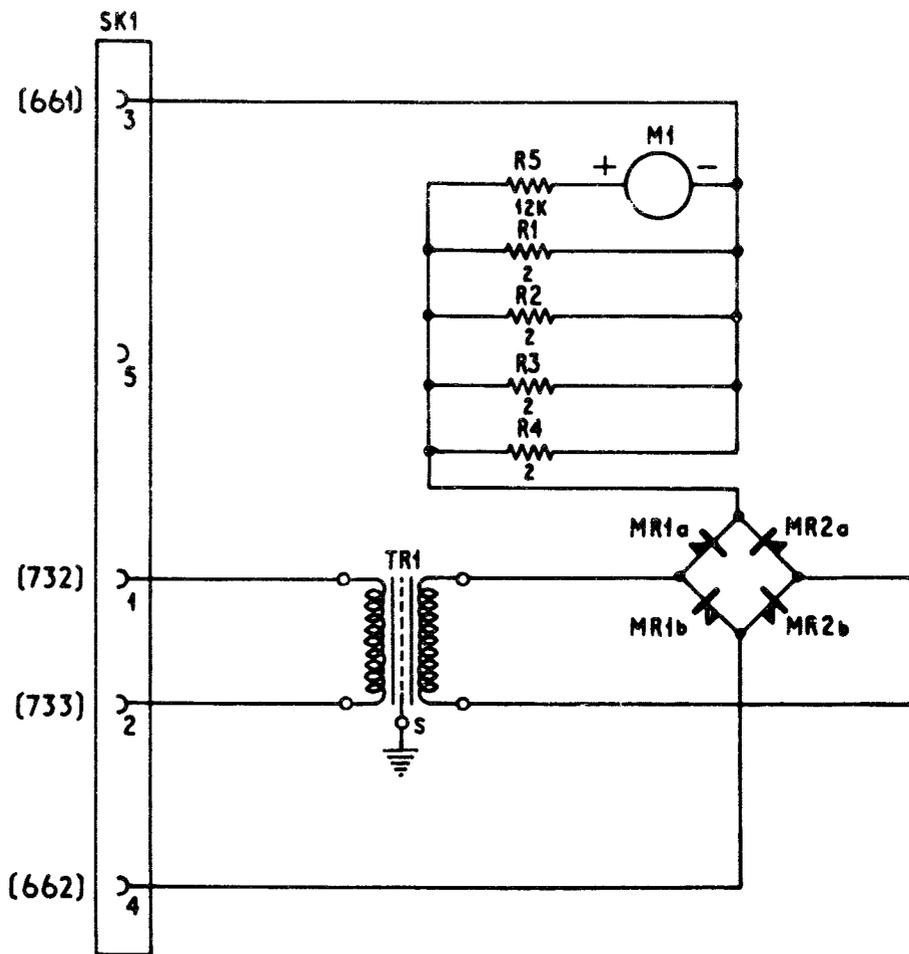


AIR DIAGRAM
6264P/MIN.
PREPARED BY MINISTRY OF SUPPLY
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ISSUE 1

SRI. 18118, FGRI. 18119 — Aerial speed control unit (YR) - circuit

Fig. 33
(A.L.15, Dec 58)

MISCELLANEOUS	SK1	R5	R1	R2	M1	MR1a	MR2a
		TR1	R3	R4		MR1b	MR2b



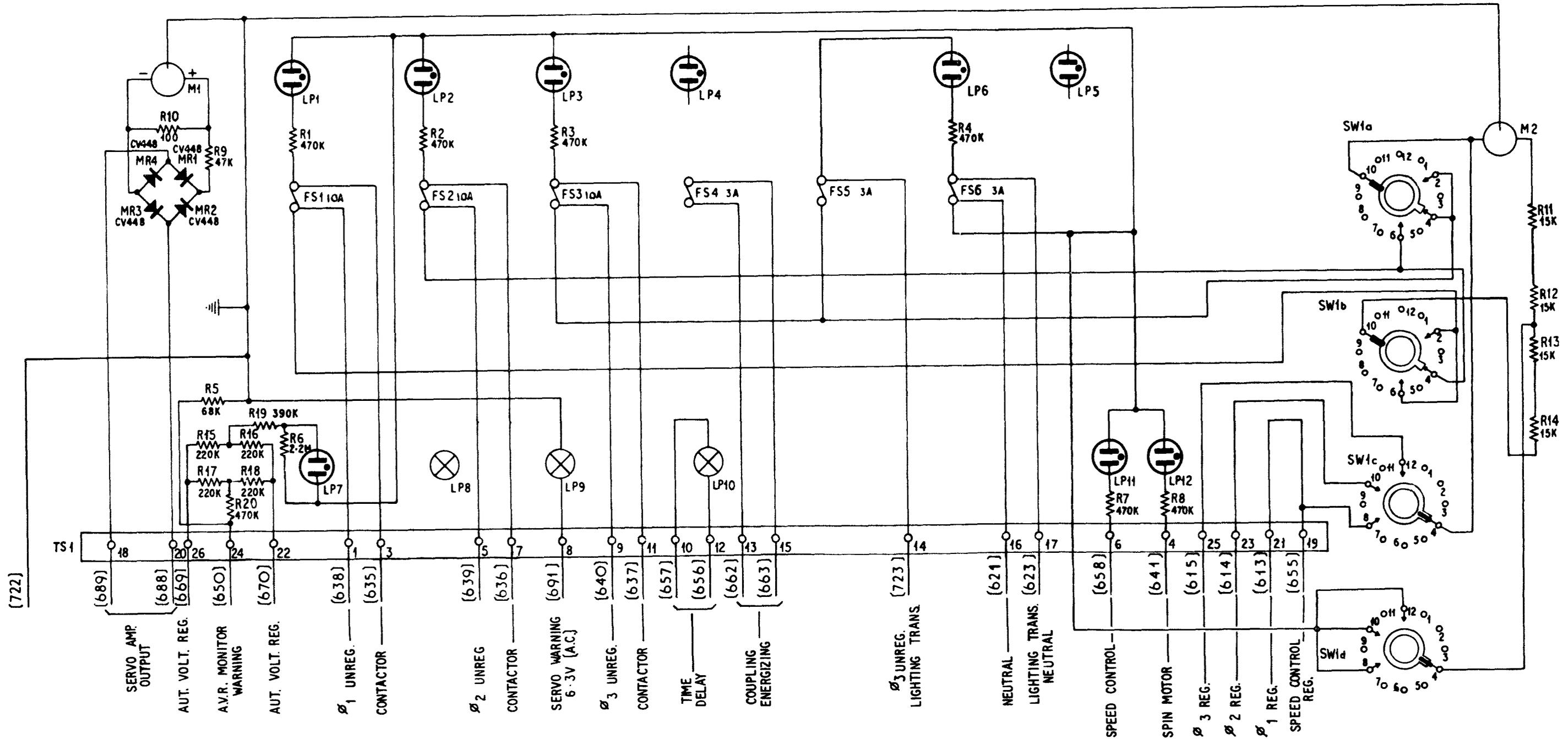
AIR DIAGRAM
6264AA/MIN.

ISSUE 1

PREPARED BY MINISTRY OF SUPPLY
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AIR MINISTRY ADMIRALTY

Fig. .34 SRI 18118, FGRI. 18119
Aerial speed rectifier unit (YS) - circuit

RESISTORS	R10 R9 R5,R15,R16,R17,R18,R19,R6,R1	R2	R3		R4	R7	R8	R14 R13 R11 R12
MISCELLANEOUS	MR4 MR3 M1 MR1MR2 R20 LP1 FS1 LP7	LP2 FS2 LP8	LP3 FS3 LP9	LP4 FS4 LP10	FSS	FS6 LP6	LP5 LP41 LP42	SW1a SW1b SW1c SW1d M2

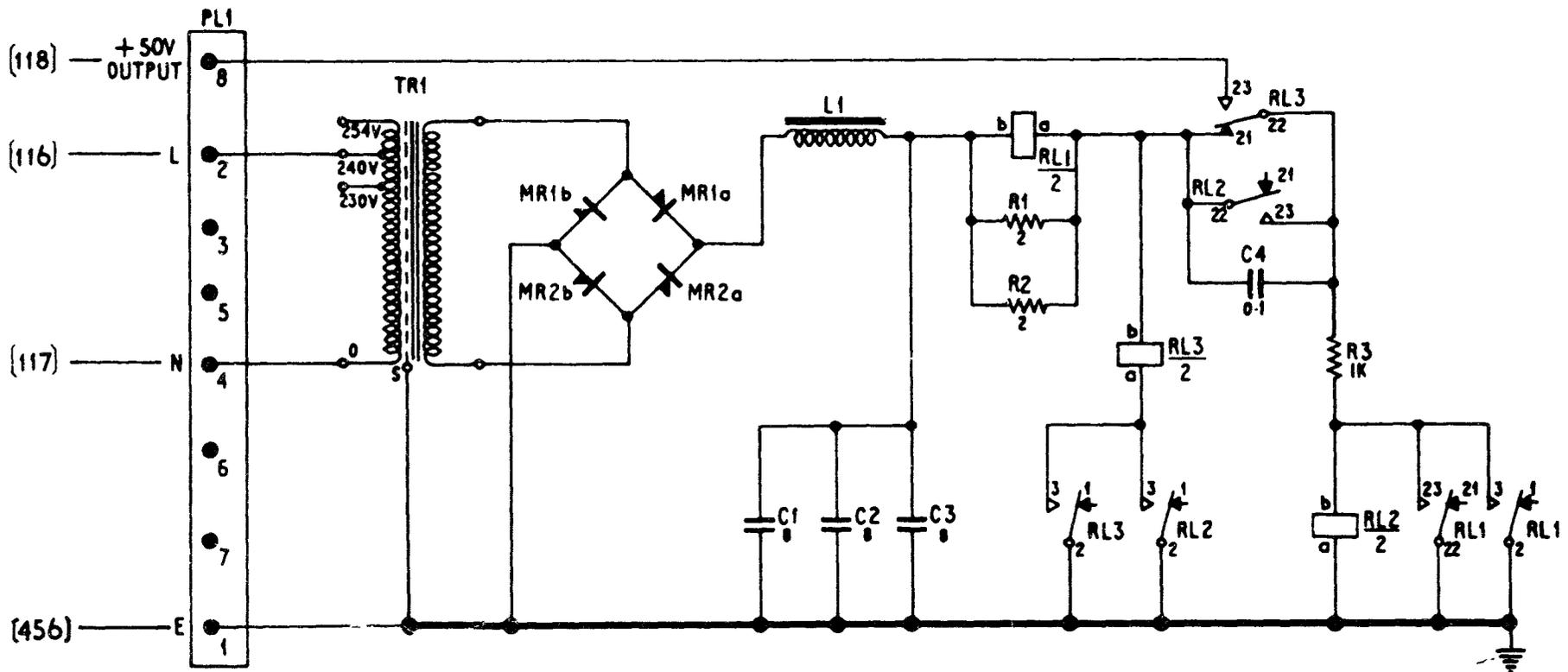


SRI 18118, FGRI 18119
Meter panel (YJ) - circuit

Fig. 35

AIR DIAGRAM
626 4E/MIN.
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AIR MINISTRY ADMIRALTY

RESISTORS						R1	R2		R3
CAPACITORS					C1	C2	C3		C4
MISCELLANEOUS	PL1	TR1	MR1	MR2	L1	RL1	RL3		RL2



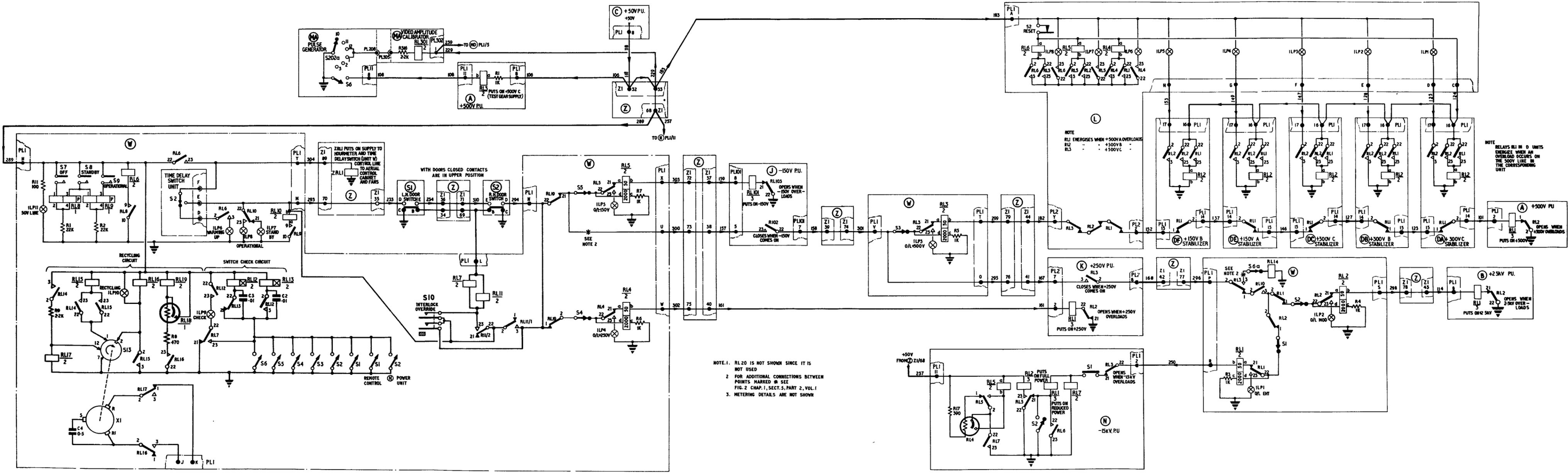
AIR DIAGRAM
6264K/MIN.

PREPARED BY MINISTRY OF SUPPLY
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ISSUE 1

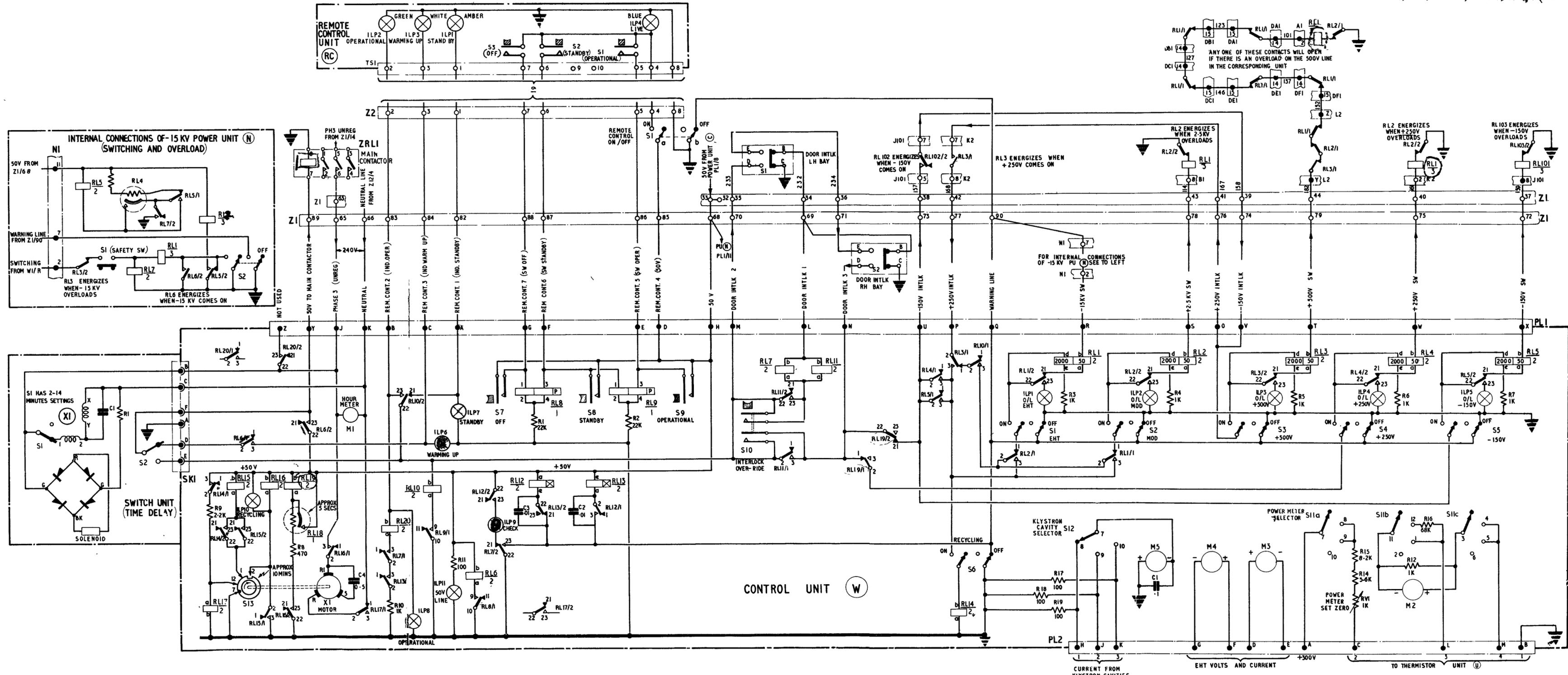
SRI 18118, FGRI 18119
+ 50V power unit (C)- circuit

Fig. 36



AIR DIAGRAM
6264 AU/MIN.
ISSUE 1

SRI. 18118 FGRI. 18119. 50V power supply warning and interlock circuit



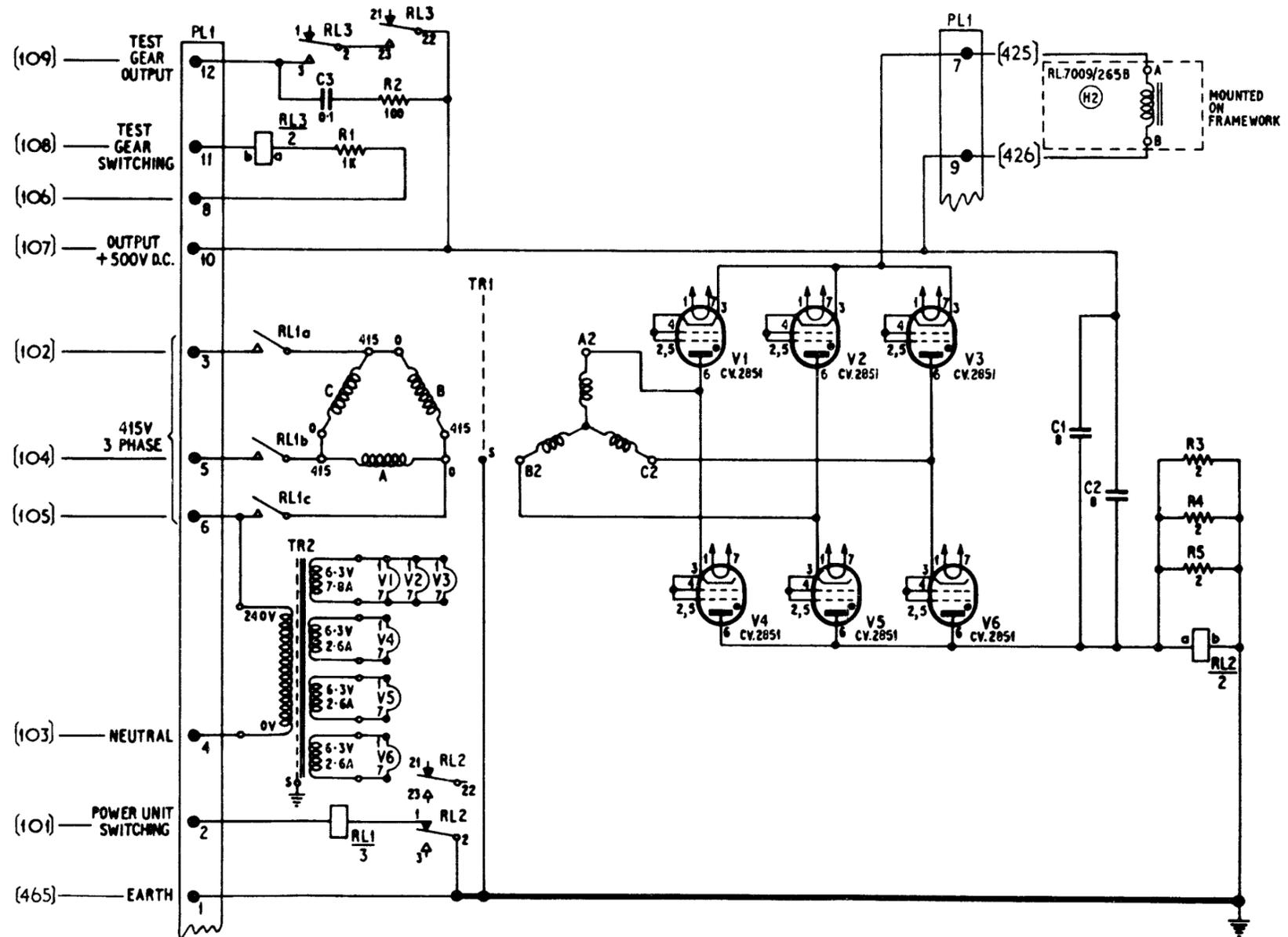
AIR DIAGRAM
6264AK/MIN.
ISSUE 1 PREPARED BY MINISTRY OF SUPPLY FOR PROMULGATION BY AIR MINISTRY

SRI.18118 and FGRI.18119 - Control units (W and RC) - circuit and external connections

Fig 38
(A.L.13, Nov. 58)

Page 57 Para 232.

RESISTORS	R1	R2	R3, R4, R5
CAPACITORS	C3		C1 C2
VALVES		V1, V4	V2, V5
			V3, V6
MISCELLANEOUS	PL1	RL3	TR1
		RL1a, RL1b, RL1c	PL1
		TR2	RL1
			RL2



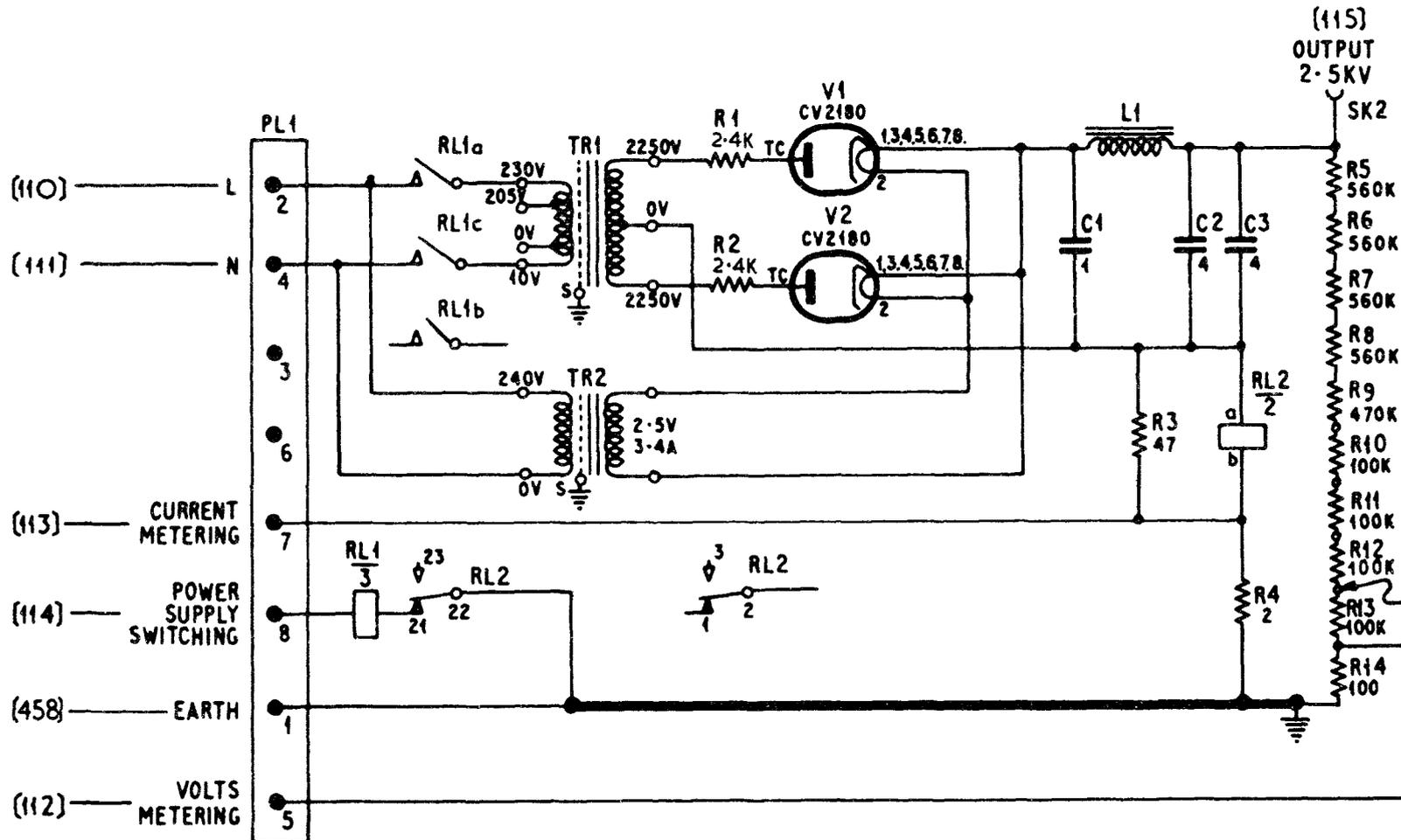
AIR DIAGRAM
6264Y/MIN.
ISSUE 1

SRI. 18118, FGRI. 18119
+500V power unit (A)-circuit
RESTRICTED

Fig. 41

(A.L.5, Oct. 58)

RESISTORS	R1	R2	R3	R4	R5	R6	R7	R8	R9
CAPACITORS	C1	C2	C3						
MISCELLANEOUS	PL1	RL1	RL1a	RL1c	RL1b	TR1	TR2	V1	V2
								L1	RL2
									SK2

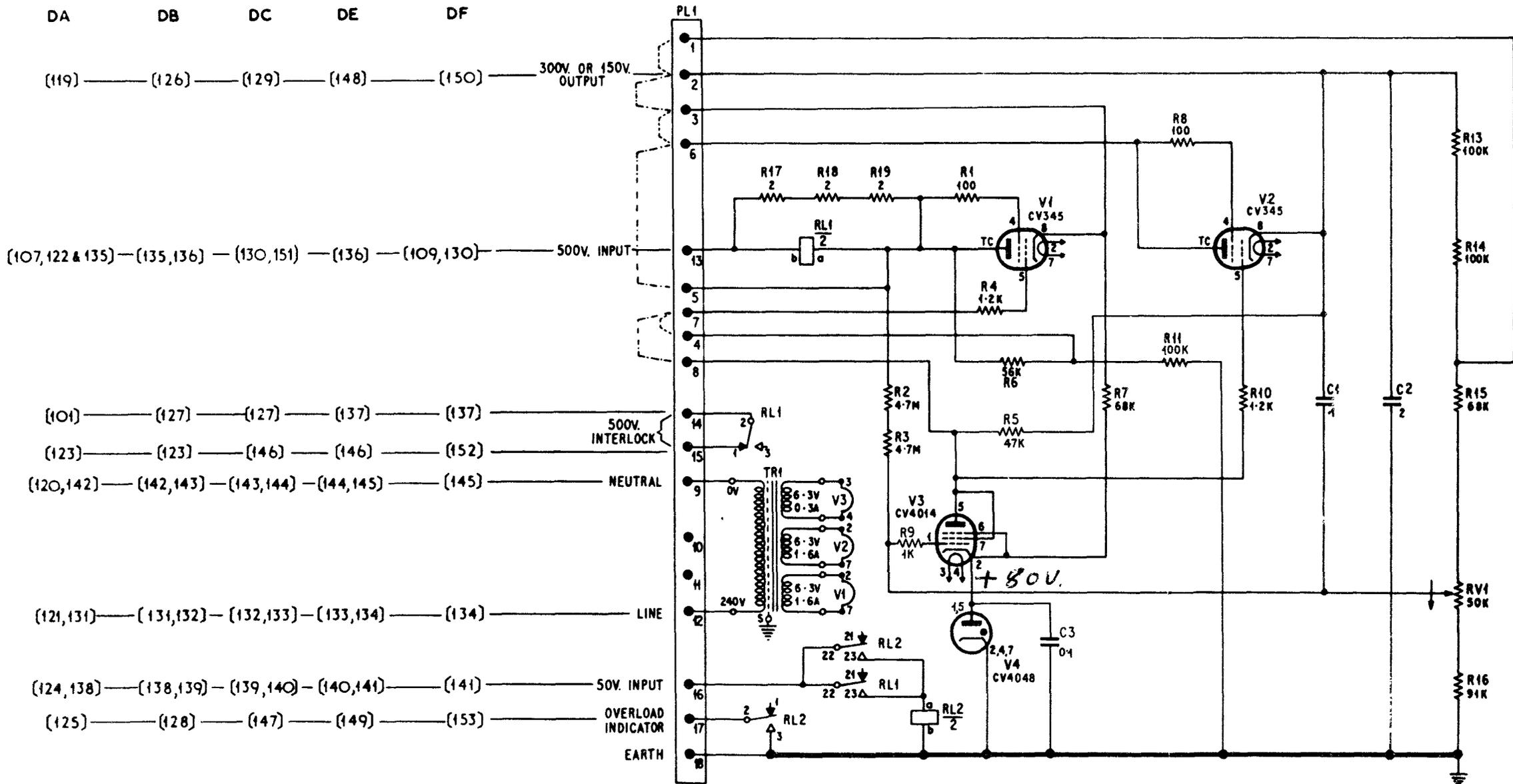


AIR DIAGRAM
6264Z/MIN.

SRI. 18118, FGRI. 18119 +2.5KV power unit (B)-circuit

Fig 42

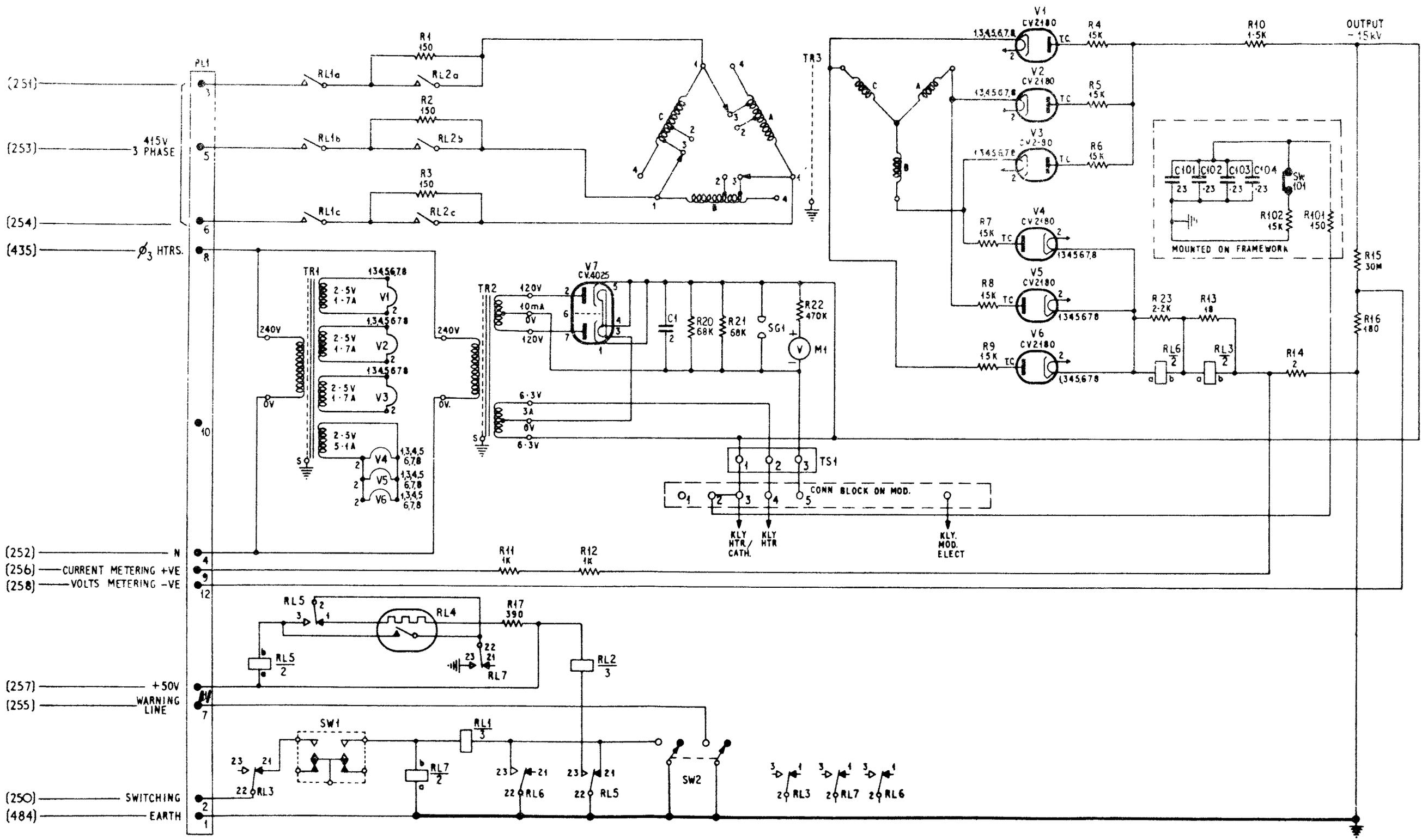
RESISTORS	R17	R18	R19	R9	R1	R4	R8	R13	R14
			R2	R3	R5	R6	R11	R10	R15
CAPACITORS							C3	C1	C2
MISCELLANEOUS	PL1	TR1	RL1	RL2	V3	V4	V1	V2	



WHEN IN USE FOR
 (1) 300V OUTPUT, EXTERNAL CONNECTIONS ARE SHOWN THUS — — — POS'S DA, DB, DC.
 (2) 150V OUTPUT, EXTERNAL CONNECTIONS ARE SHOWN THUS - - - - - POS'S DE, DF.

SRI. 18118, FGRI. 18119
 +300V or +150V voltage stabilizer (D) - circuit

RESISTORS	R1 R2 R3	R4 R17	R12	R20 R21	R22	R7 R8 R9	R4 R5 R6	R23	R13 R10 R102 R14 R101 R15 R16
CAPACITORS				C1				C101 C102 C103 C104	
MISCELLANEOUS	PL1	TR1 RL1a RL1b RL1c RL2a RL2b RL2c TR2	V7		SG1 M1 TR3	V1 V2 V3 V4 V5 V6	RL6 RL3	SW101	
		RL5 SW1 RL4 RL7 RL1	RL2	SW2	TS1				

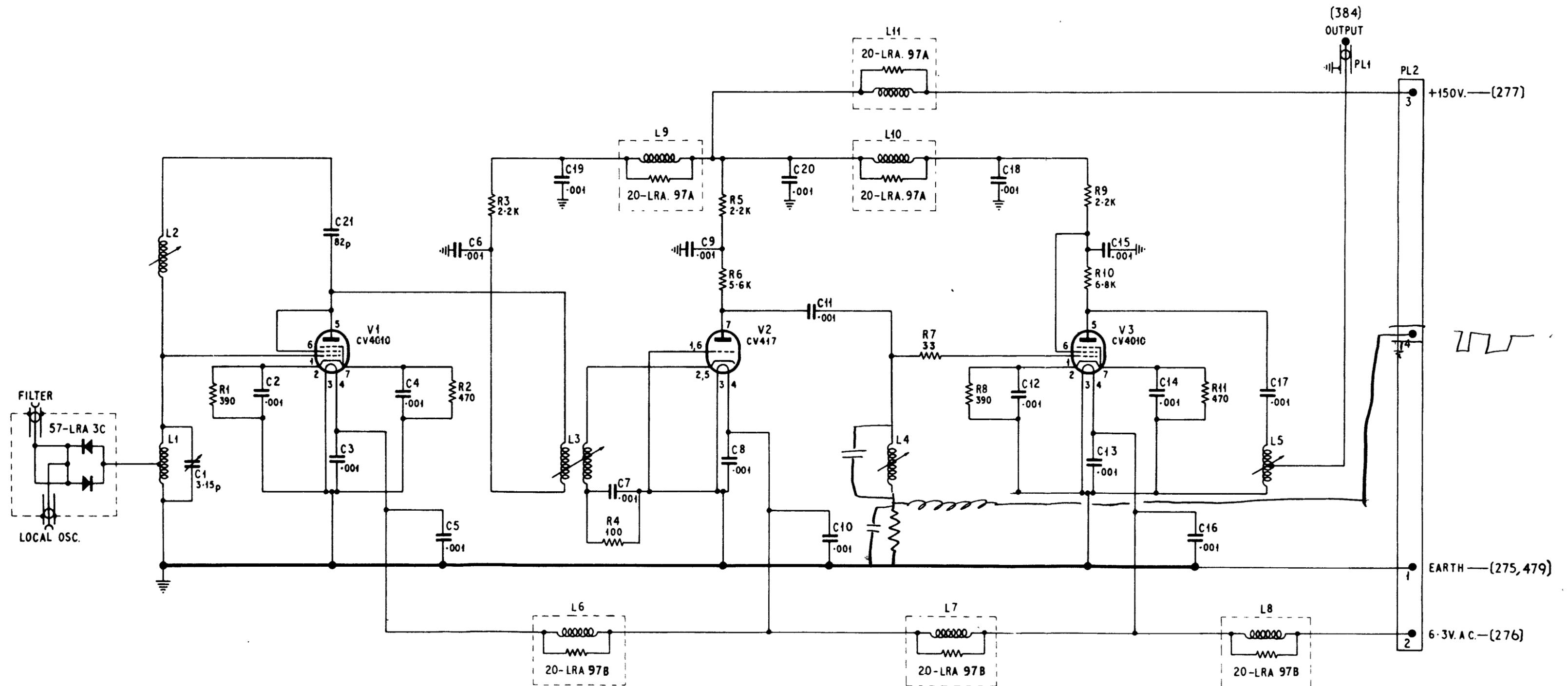


- (251) —
- (253) — 415V 3 PHASE
- (254) —
- (435) — ϕ_3 HTRS.
- (252) — N
- (256) — CURRENT METERING +VE
- (258) — VOLTS METERING -VE
- (257) — +50V
- (255) — WARNING LINE
- (250) — SWITCHING
- (484) — EARTH

AIR DIAGRAM
6264L/MIN.
ISSUE 1
PREPARED BY: MINISTRY OF SUPPLY
REVISION: 10/1/50
BY: J. H. G. (A.L.5)

SRI. 18118, FGRI. 18119 — -15KV power unit (N)-circuit

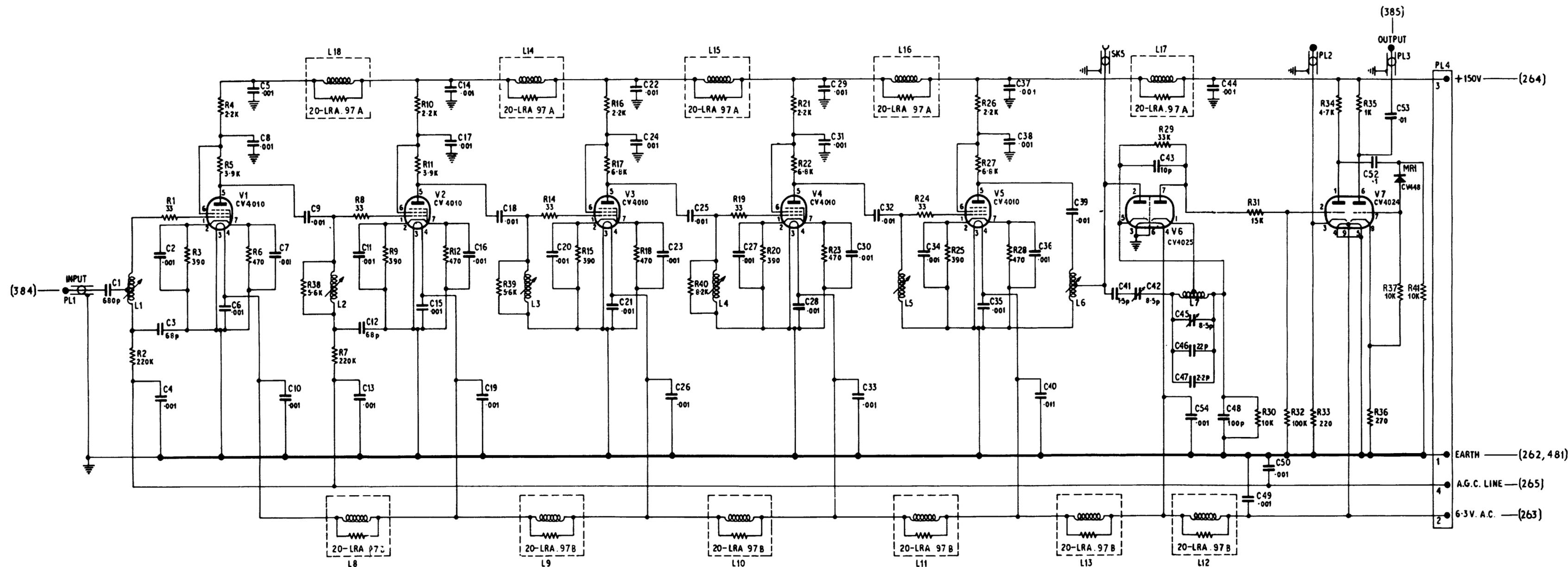
RESISTORS	R1	R2	R3	R4	R5	R6	R7	R8	R9	R10	R11						
CAPACITORS	C1	C2	C3	C4	C5	C6	C7	C8	C9	C10	C11	C12	C13	C14	C15	C16	C17
MISCELLANEOUS	L2	L1	V1	L3	L6	L9	V2	LH	L4	L10	L7	V3	L5	L8	PL1	PL2	



AIR DIAGRAM
6264U/MIN.
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SRI. 18118, FGRI. 18119 - IF pre-amplifier - circuit

RESISTORS	R2	R1	R3	R4	R5	R6	R38	R7	R8	R9	R10	R11	R12	R39	R4	R15	R16	R17	R18	R40	R19	R20	R21	R22	R23	R24	R25	R26	R27	R28	R29	R31	R30	R32	R33	R34	R35	R36	R37	R41													
CAPACITORS	C1	C2	C3	C4	C6	C5	C8	C7	C10	C9	C11	C12	C13	C15	C14	C17	C16	C19	C18	C20	C21	C22	C24	C23	C25	C26	C27	C28	C29	C31	C30	C33	C32	C34	C35	C37	C38	C36	C40	C39	C41	C42	C43	C45	C46	C47	C44	C54	C48	C49	C50	C53	C52
MISCELLANEOUS	PL1	L1		V1				L2	L18	L8	V2			L3	L14	L9	V3			L4	L15	L10	V4			L5	L16	L11	V5			L6	L13	SK5	V6	L17	L7	L12											PL2	V7	PL3	MR1	PL4



AIR DIAGRAM
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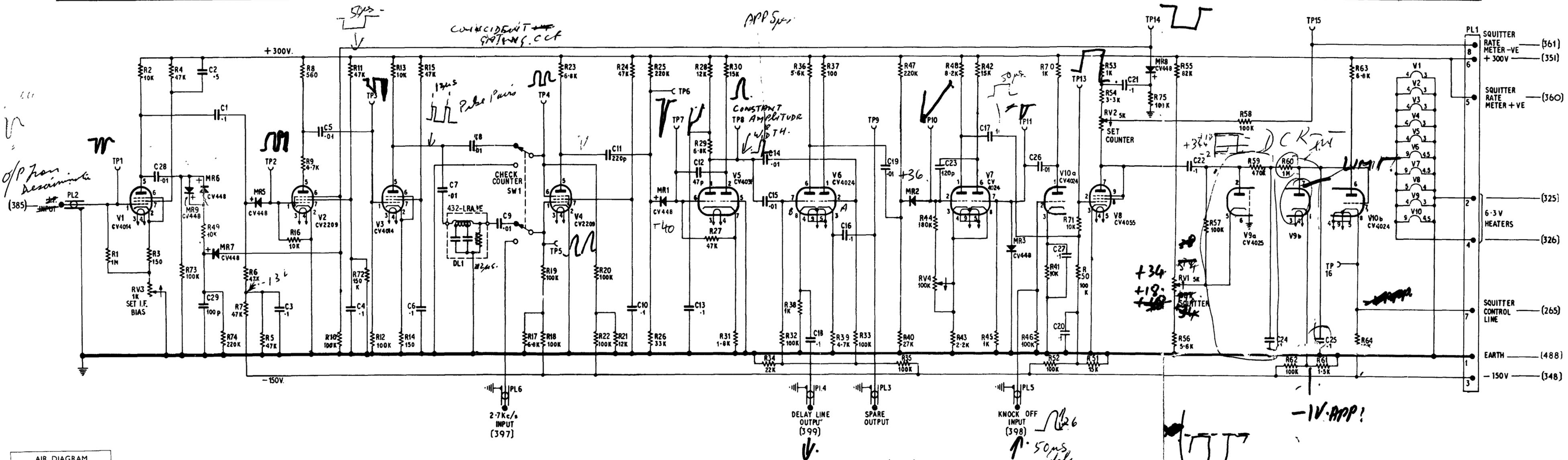
SRI. 18118, FGRI. 18119 — IF amplifier (P) - circuit

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Fig. 47

(A.L.16, Dec. 58)

RESISTORS	R1	R2	R3	R4	R5	R6	R7	R8	R9	R10	R11	R12	R13	R14	R15	R16	R17	R18	R19	R20	R21	R22	R23	R24	R25	R26	R27	R28	R29	R30	R31	R32	R33	R34	R35	R36	R37	R38	R39	R40	R41	R42	R43	R44	R45	R46	R47	R48	R49	R50	R51	R52	R53	R54	R55	R56	R57	R58	R59	R60	R61	R62	R63	R64
CAPACITORS	C1	C2	C3	C4	C5	C6	C7	C8	C9	C10	C11	C12	C13	C14	C15	C16	C17	C18	C19	C20	C21	C22	C23	C24	C25	C26	C27	C28	C29	C30	C31	C32	C33	C34	C35	C36	C37	C38	C39	C40	C41	C42	C43	C44	C45	C46	C47	C48	C49	C50	C51	C52	C53	C54	C55	C56	C57	C58	C59	C60	C61	C62	C63	C64
VALVES	V1	V2	V3	V4	V5	V6	V7	V8	V9a	V9b	V10a	V10b																																																				
TEST POINTS	TP1	TP2	TP3	TP4	TP5	TP6	TP7	TP8	TP9	TP10	TP11	TP13	TP14	TP15	TP16																																																	
MISCELLANEOUS	PL2	MR9	MR6	MR7	MR5	MR4	PL7	DL1	PL6	SW1	MR1	PL4	PL3	MR2	MR3	PL5	MR8	PL1																																														

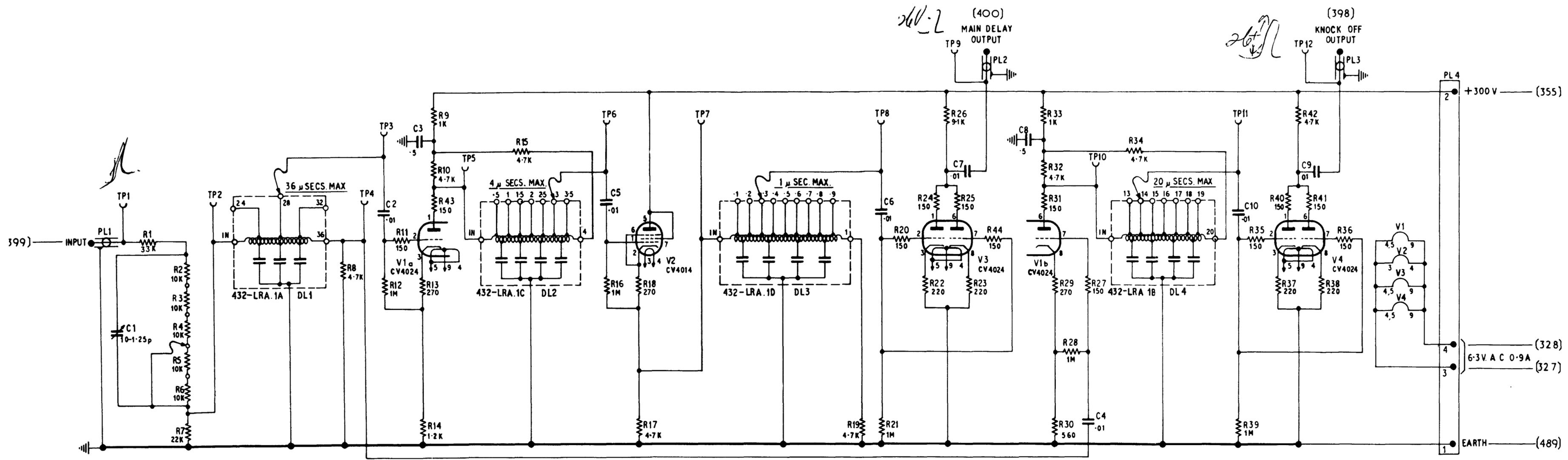


SRI. 18118, FGRI. 18119 - Video decoder (XC) - circuit

AIR DIAGRAM
6264V/MIN.
PREPARED BY MINISTRY OF SUPPLY
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924/8/6867/28706/253694/375/11/62

RESISTORS	R1 R2 R3 R4 R5 R6 R7	R8 R12 R11	R13 R9 R14 R43	R15	R16	R18 R17	R19 R21	R24 R26 R25 R22 R23 R44	R33 R32 R31	R34	R35 R40 R42 R41	
CAPACITORS	C1	C2 C3	C5	C6	C7	C8	C4	C10	C9			
TEST POINTS	TP1	TP2	TP4 TP3	TP5	TP6	TP7	TP8	TP9	TP10	TP11	TP12	
MISCELLANEOUS	PL1	DL1	V1a	DL2	V2	DL3	V3	PL2	V1b	DL4	V4 PL3	PL4



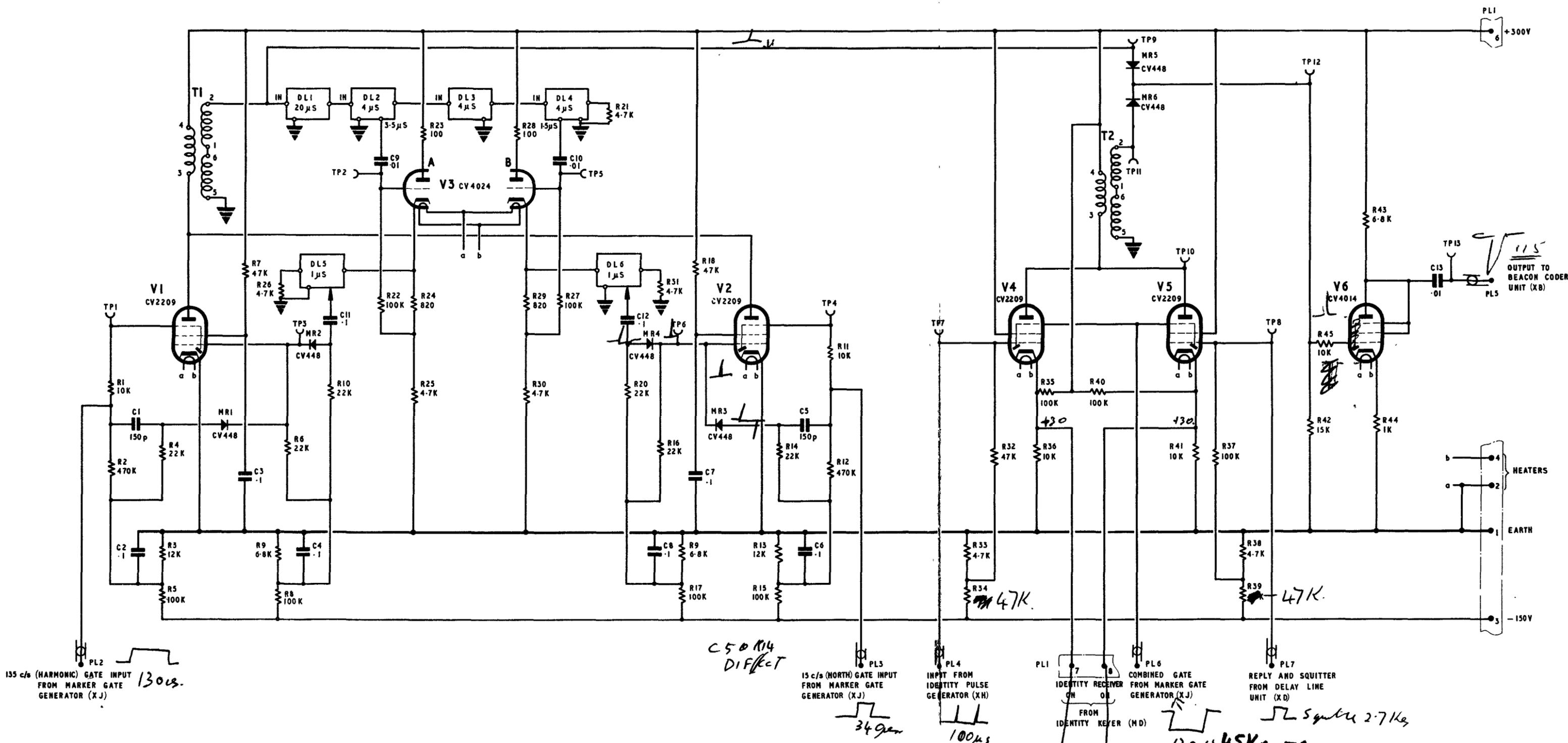
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6264AE/MIN.
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AIR MINISTRY ADMIRALTY

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SRI. 18118, FGRI. 18119 - Delay line unit (XD) - circuit

Fig. 4y
(A.L.16, Dec. 58)

RESISTORS	R1 R2	R4 R3 R5	R7	R26 R6 R9 R8	R10	R22	R23 R24 R25	R28 R29 R30	R27	R21 R20	R31 R16 R19 R17 R18	R14 R13 R15	R12	R33 R34	R32	R35 R36	R40	R41 R37 R39 R38	R42 R45	R43 R44	
CAPACITORS	C1 C2		C3	C4	C11	C9			C10	C12	C8	C7	C5 C6								C13
MISCELLANEOUS	PL2 TP1	VI	DL1 DL5	DL2	V3A	DL3	V3B	DL4	DL6	MR4 TP6	MR3	V2	TP4 PL3	PL4	V4	TP6 MR5 MR6	V5	PL7	V6	TP13	
		TI MRI	TP3 MR2											TP7		TP9 MR5 MR6	V5	TP8			



135 c/s (HARMONIC) GATE INPUT FROM MARKER GATE GENERATOR (XJ) 130 us.

C5 & R14 DIFFER

15 c/s (NORTH) GATE INPUT FROM MARKER GATE GENERATOR (XJ) 349 us

INPUT FROM IDENTITY PULSE GENERATOR (XH) 100 us at 2.7 Kc

IDENTITY RECEIVER ON OFF FROM IDENTITY KEYS (MD) 7.5 sec coding every 37.5 sec.

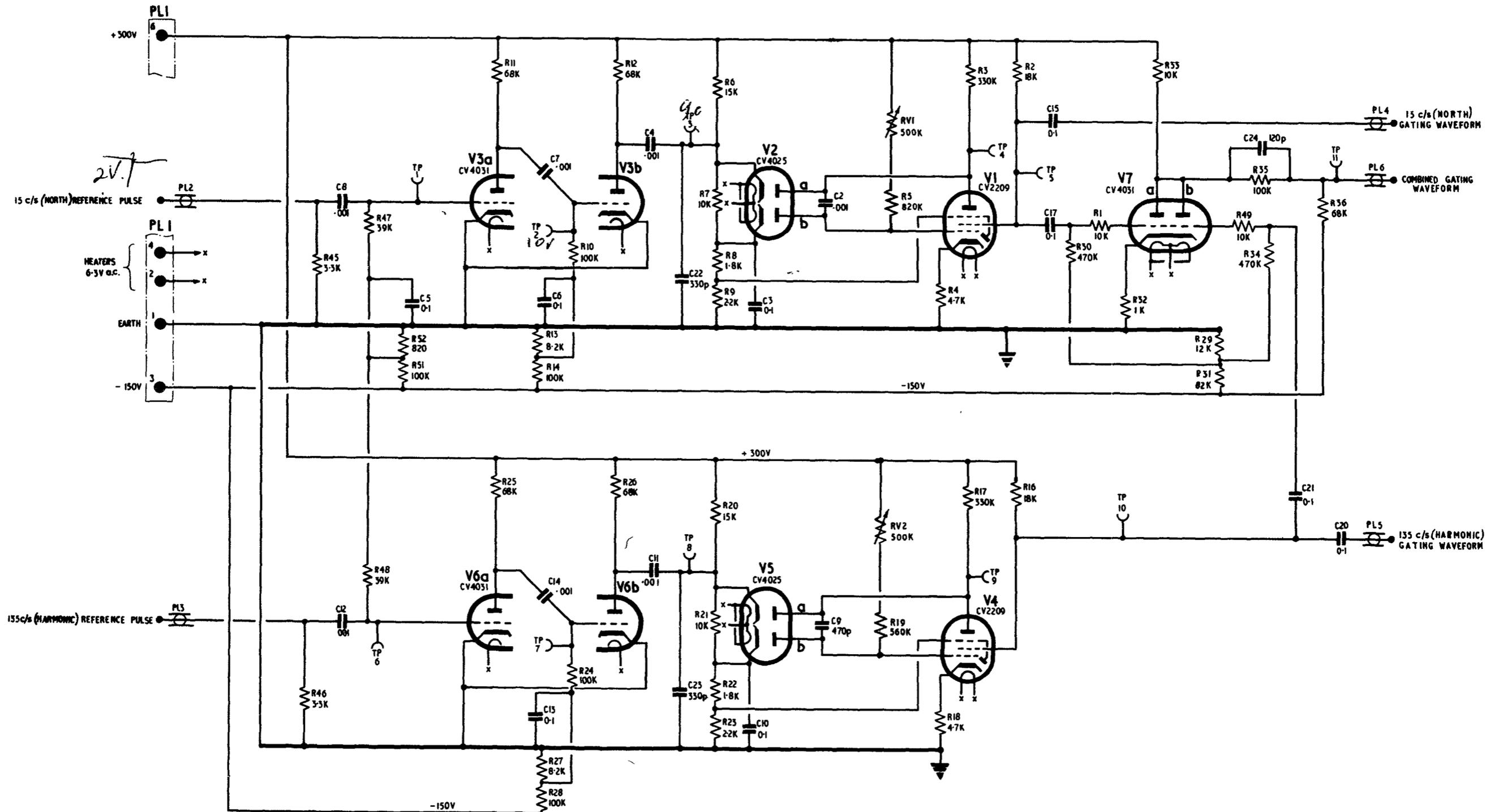
COMBINED GATE FROM MARKER GATE GENERATOR (XJ) 130 us 45V to TP11 or 340m. XT. Names superimposed on +16V Voltage (30V APP)

REPLY AND SQUITTER FROM DELAY LINE UNIT (XD) 5 symbols 2.7 Kc

SRI. 18118, FGRI. 18119: Priority mixer (XE)-circuit

AIR DIAGRAM
6264AF/MIN.
ISSUE 3

Fig. 50

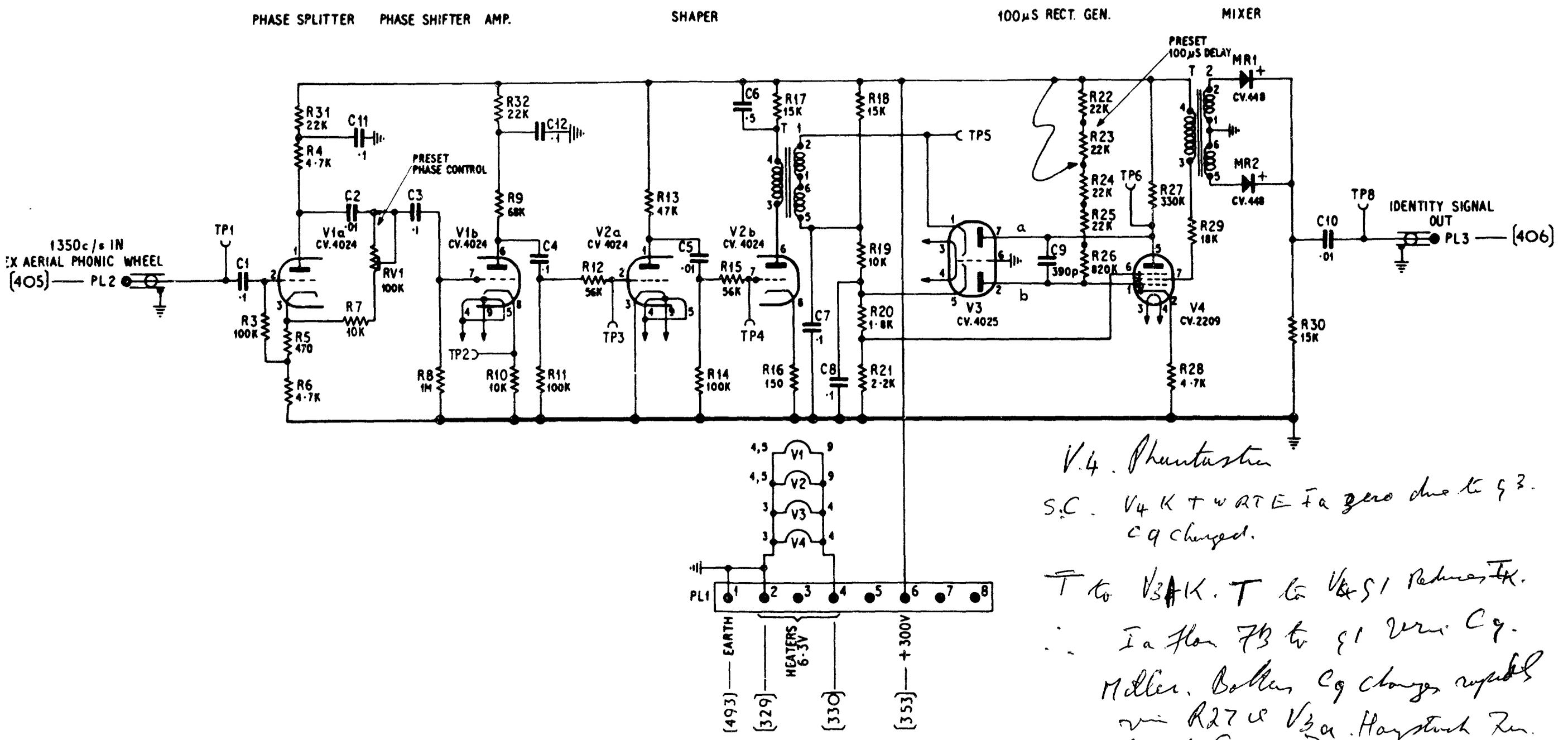


AIR DIAGRAM
 6264 D/MIN.
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 ISSUE 2

SRI.18118, FGRI.18119 - Marker gate generator (XJ)-circuit

Fig.51

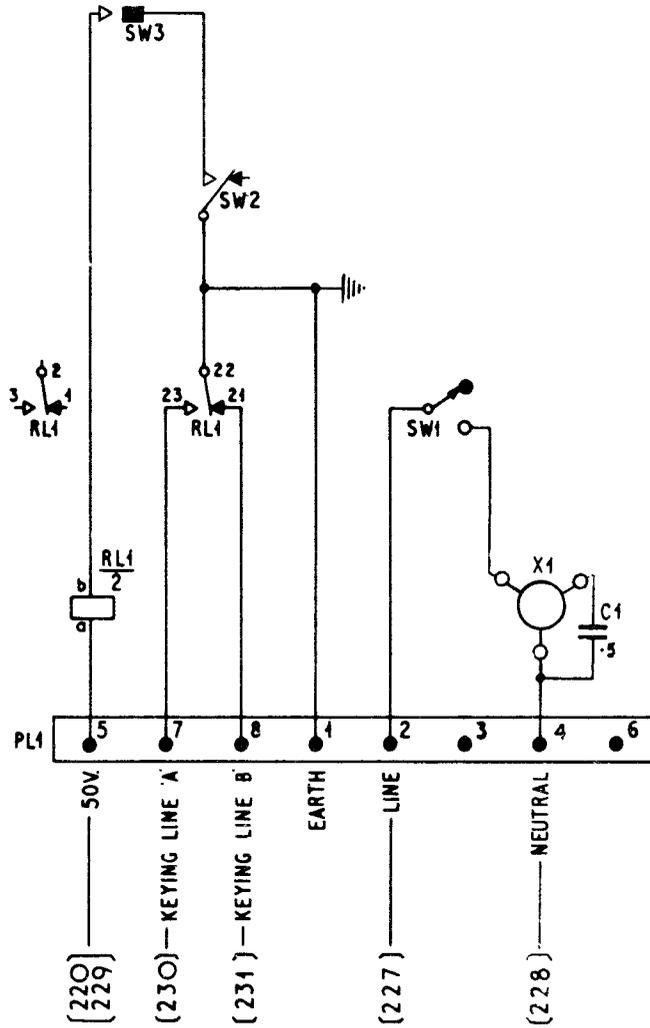
RESISTORS	R3	R5 R4 R31	RV1	R8	R32 R9	R10 R11	R12	R13	R15 R14	R17	R16	R18 R20	R19 R21	R22 R24 R26	R27 R29	R28	R30
CAPACITORS	C1	C2 C11 C3		C4 C12					C5 C6	C7 C8		C9					C10
MISCELLANEOUS	PL2	TP1	V1a	TP2 V1b	TP3 V2a	TP4 V2b	T 1	PL1	V3 TP5	TP6 V4	T 2	MR1 MR2	TP8	PL3			



SRI. 18118, FGRI. 18119
Identity pulse generator (XH) - circuit

Fig.52

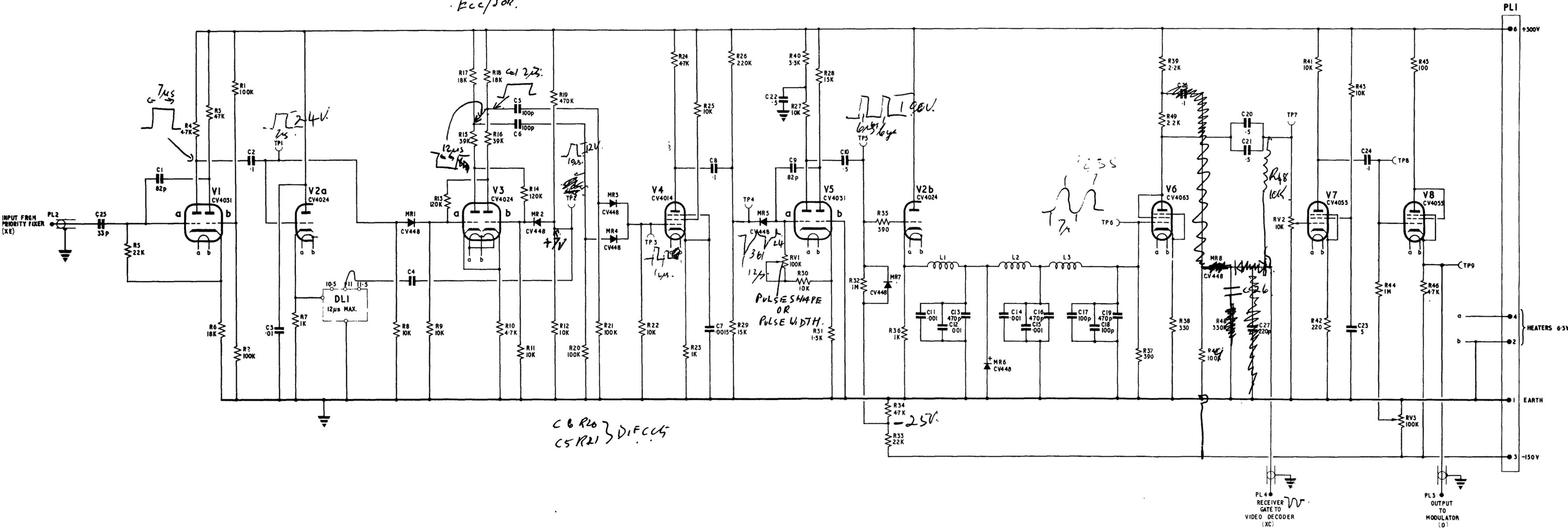
PL1 RL1 SW3 SW2 SW1 X1 C1 MISCELLANEOUS



SRI. 18118, FGRI. 18119 identity keyer(MD)-circuit Fig. 53

AIR DIAGRAM 6264H/MIN.	
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Ecc/50R.



SRI. 18118. FGRI. 18119: Beacon coder unit (XB)-circuit

Fig 54

AIR DIAGRAM
6264AG/MIN.
ISSUE 2

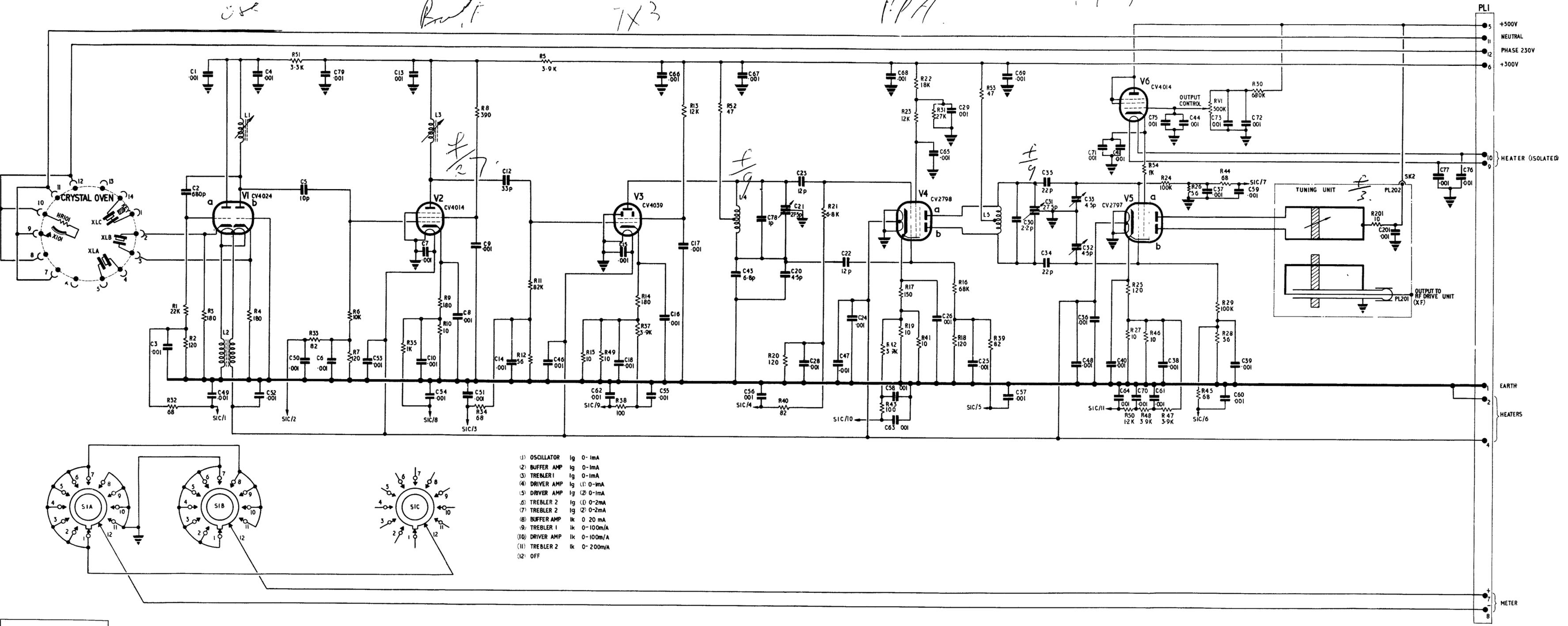
Handwritten: C.R.

Handwritten: B.M.F.

Handwritten: TX3

Handwritten: P.P.A.

Handwritten: A.P. 253



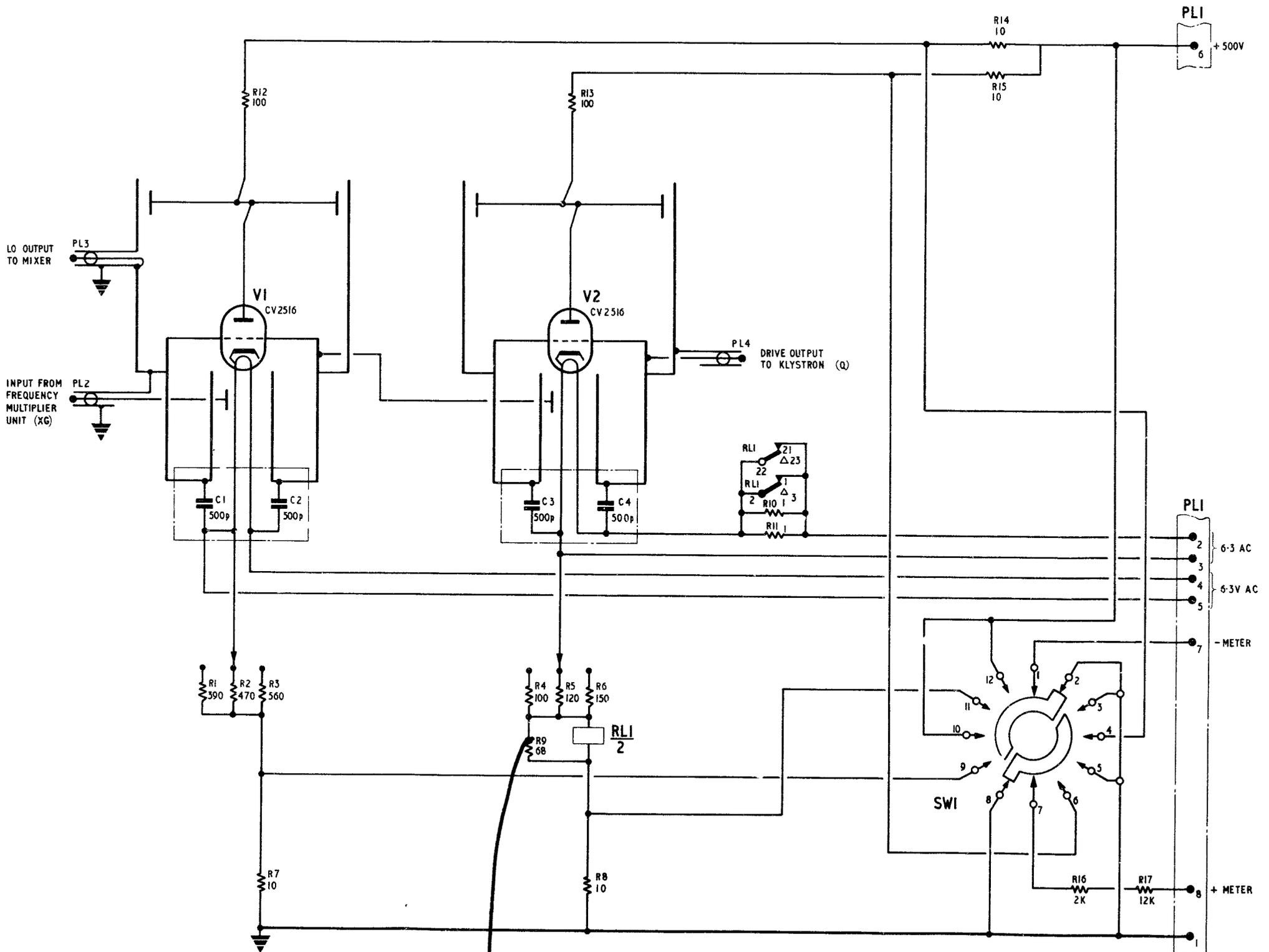
- (1) OSCILLATOR Ig 0-1mA
- (2) BUFFER AMP Ig 0-1mA
- (3) TREBLER 1 Ig 0-1mA
- (4) DRIVER AMP Ig (1) 0-1mA
- (5) DRIVER AMP Ig (2) 0-1mA
- (6) TREBLER 2 Ig (1) 0-2mA
- (7) TREBLER 2 Ig (2) 0-2mA
- (8) BUFFER AMP Ik 0-20 mA
- (9) TREBLER 1 Ik 0-100mA
- (10) DRIVER AMP Ik 0-100mA
- (11) TREBLER 2 Ik 0-200mA
- (12) OFF

AIR DIAGRAM
6264AJ/MIN.
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SRI.18118, FGRI.18119. Frequency multiplier unit (XG)-circuit

Fig.55
(A.L. II, Nov. 58)



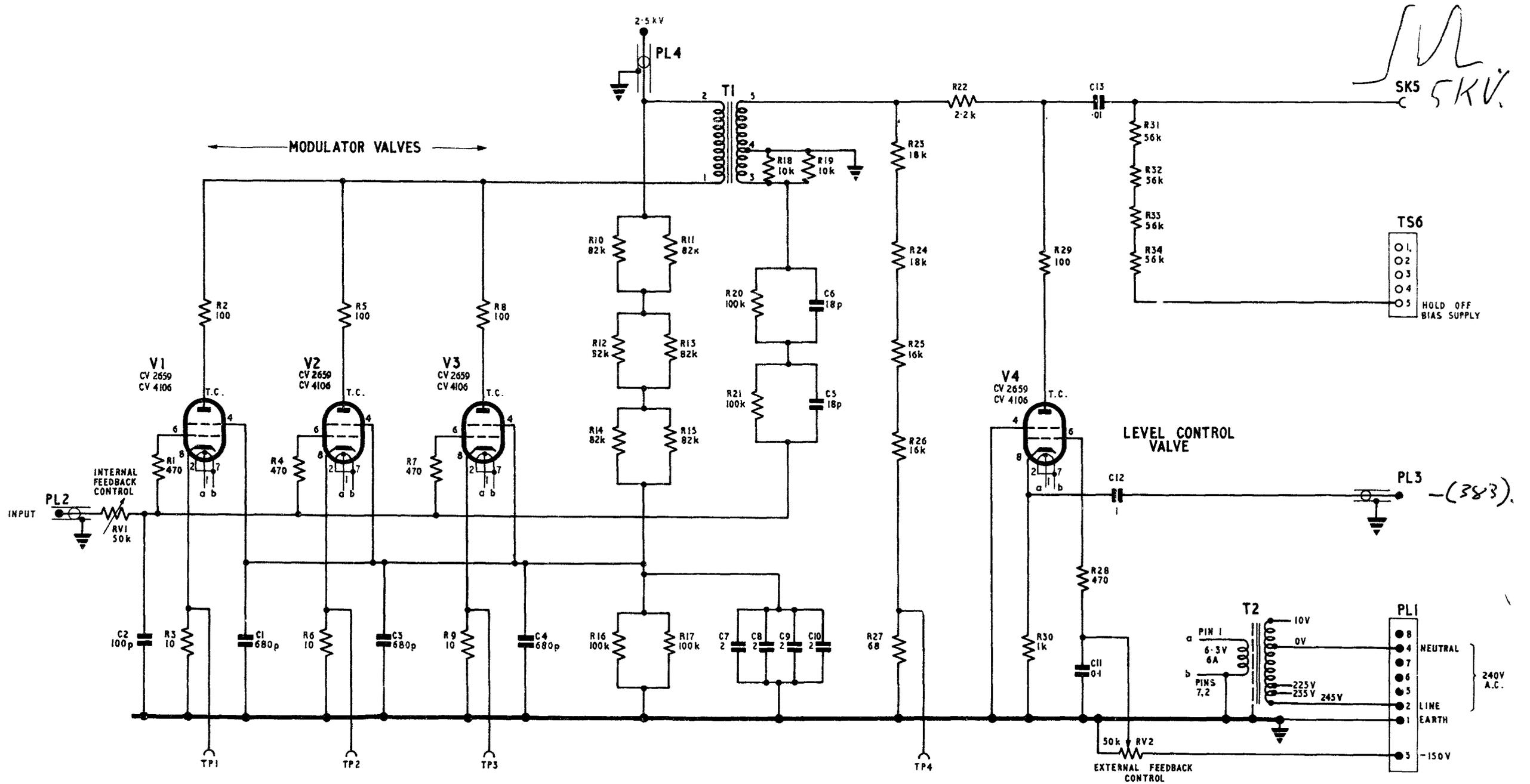
AIR DIAGRAM
6264AH/MIN.

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*Prepared by
Chatter.*
SRI. 18118, FGRI. 18119. RF drive unit (XF)-circuit

Fig 56
(A.L.II, Nov. 58)

RESISTORS	RV1	R1	R2	R4	R5	R7	R8	R10	R14	R11	R15	R20	R18	R19	R23	R25	R27	R22	R29	R28	R31	R32	R33
CAPACITORS	C2	C1	C3	C4	C7	C8	C9	C10	C6	C5	C11	C13	C12										
MISCELLANEOUS	PL2	V1	TP1	V2	TP2	V3	TP3	PL4	TI			TP4	V4								TS6	PL3	PL1



SRI. 18118, FGRI. 18119
Modulator (O): circuit

MISCELLANEOUS PL1

SW3 SW1
SW2

SK10 SK6
SK8

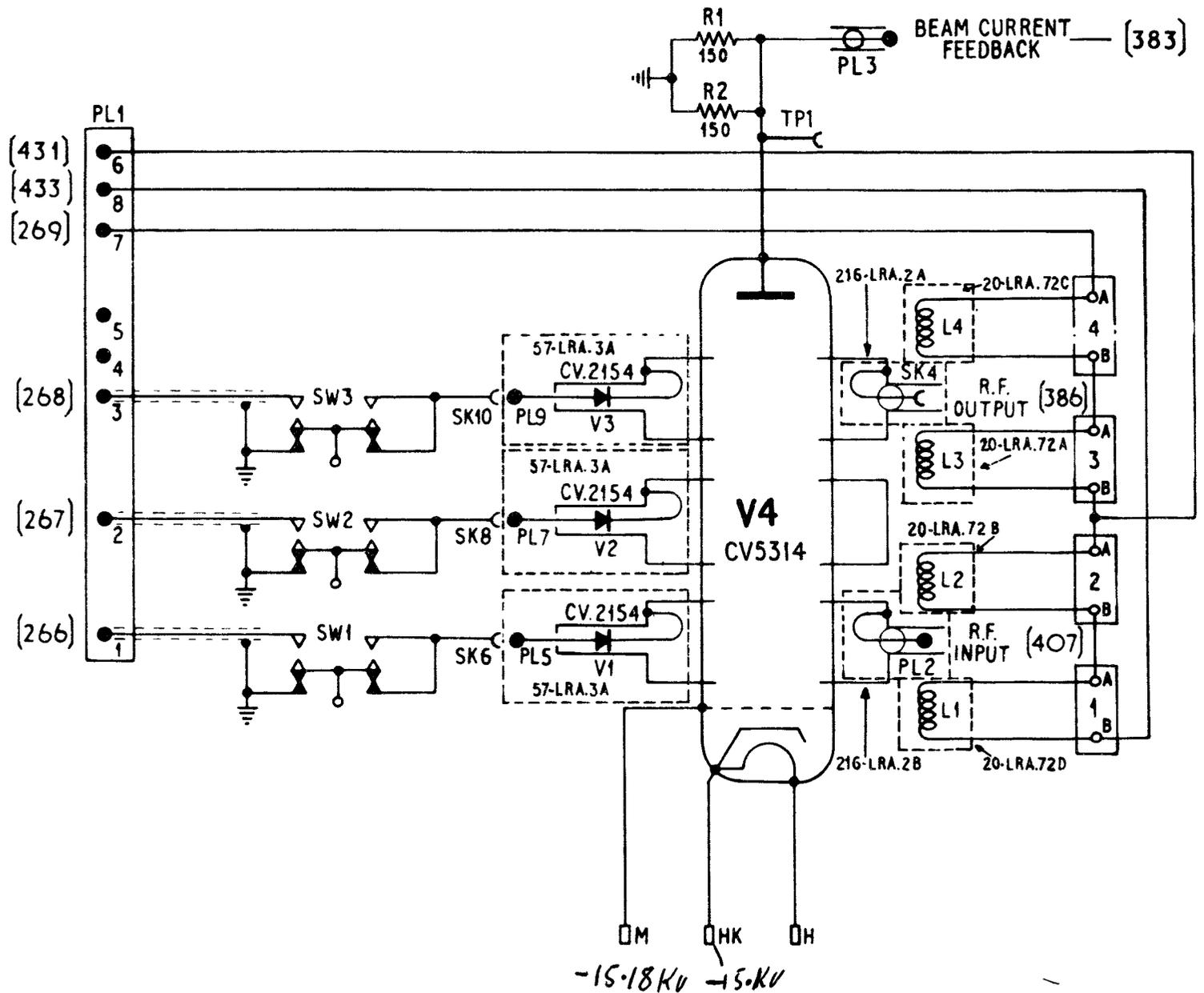
PL9 PL5
PL7

V3 V1
V2

R1 R2
V4

TP1

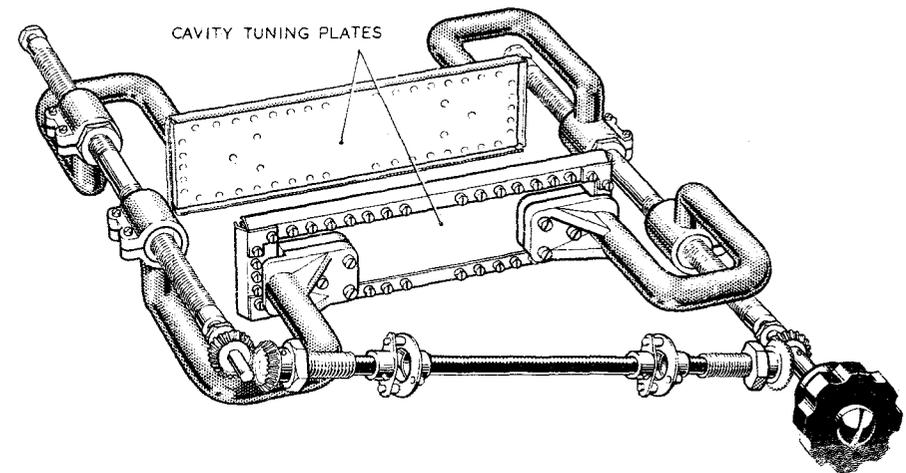
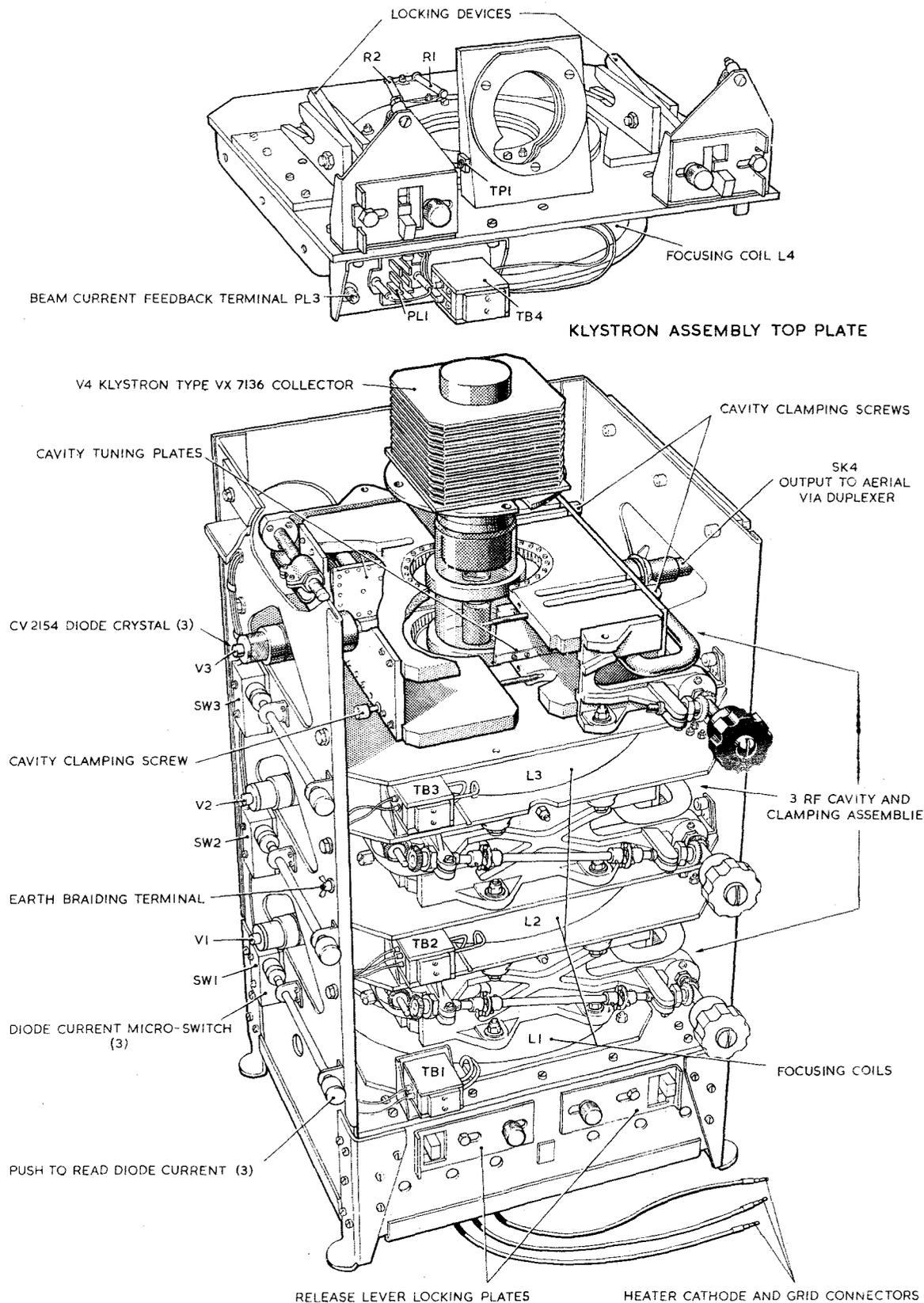
SK4 PL3 L4 L3
PL2 L2 L1



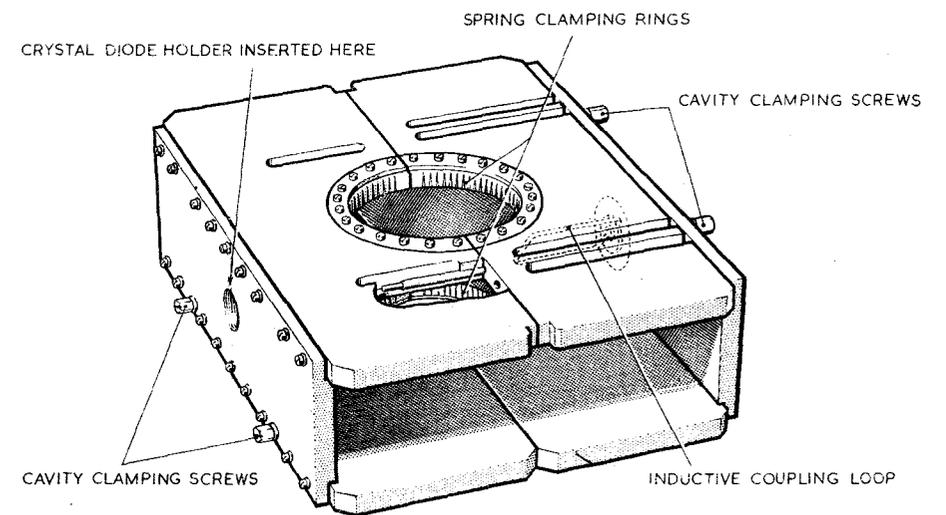
AIR DIAGRAM
6264AD/MIN.

SRI. 18118, FGRI. 18119 - Klystron assembly (Q) - circuit

Fig. 58



CAVITY TUNING MECHANISM

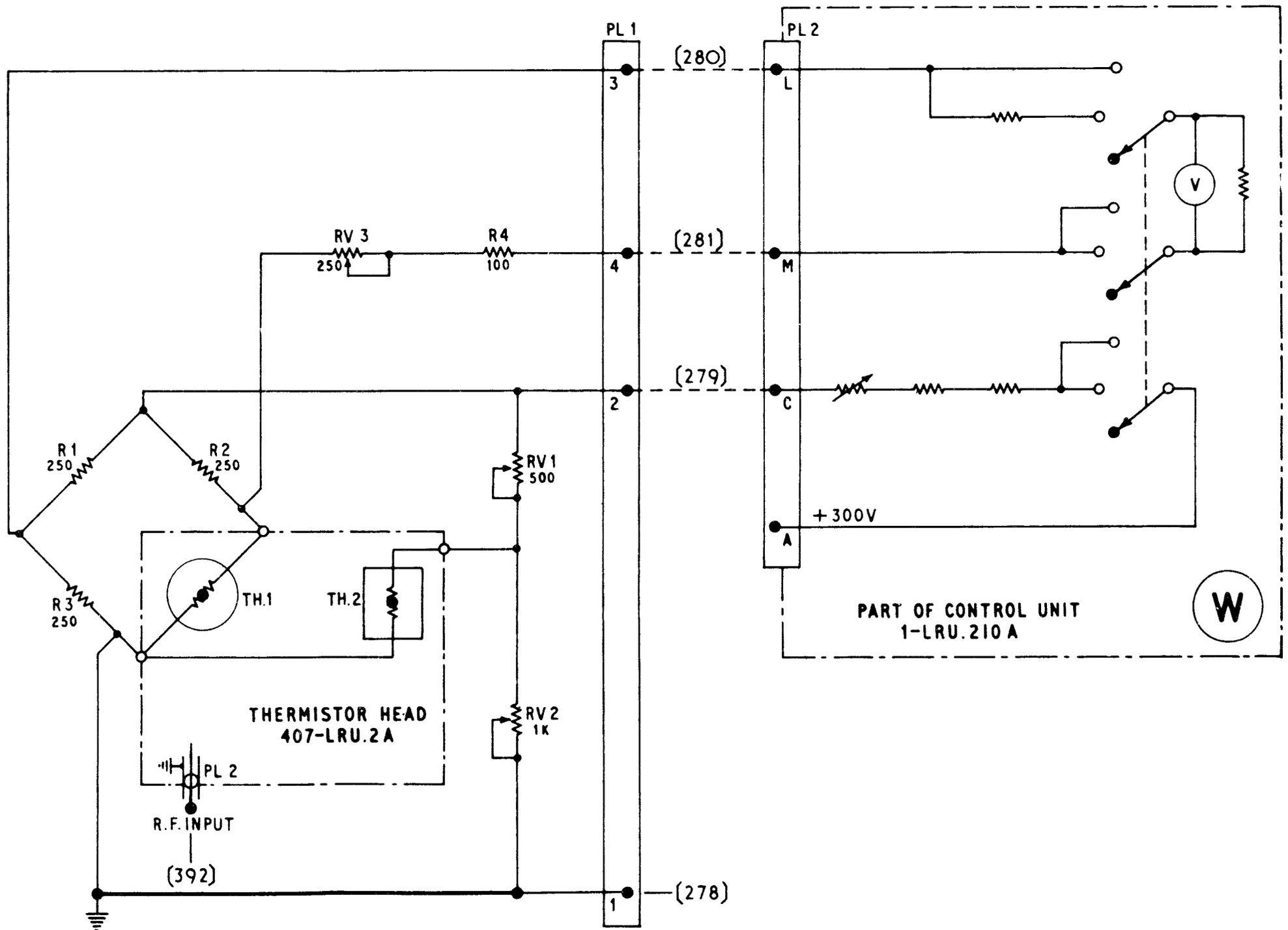


RF CAVITY AND CLAMPING ASSEMBLY

Klystron assembly (Q) - General arrangement

Fig. 59

RESISTORS	R1 R3	R2	RV3	R4	RV1 RV2
MISCELLANEOUS	PL 2	TH 1	TH 2	PL 1	

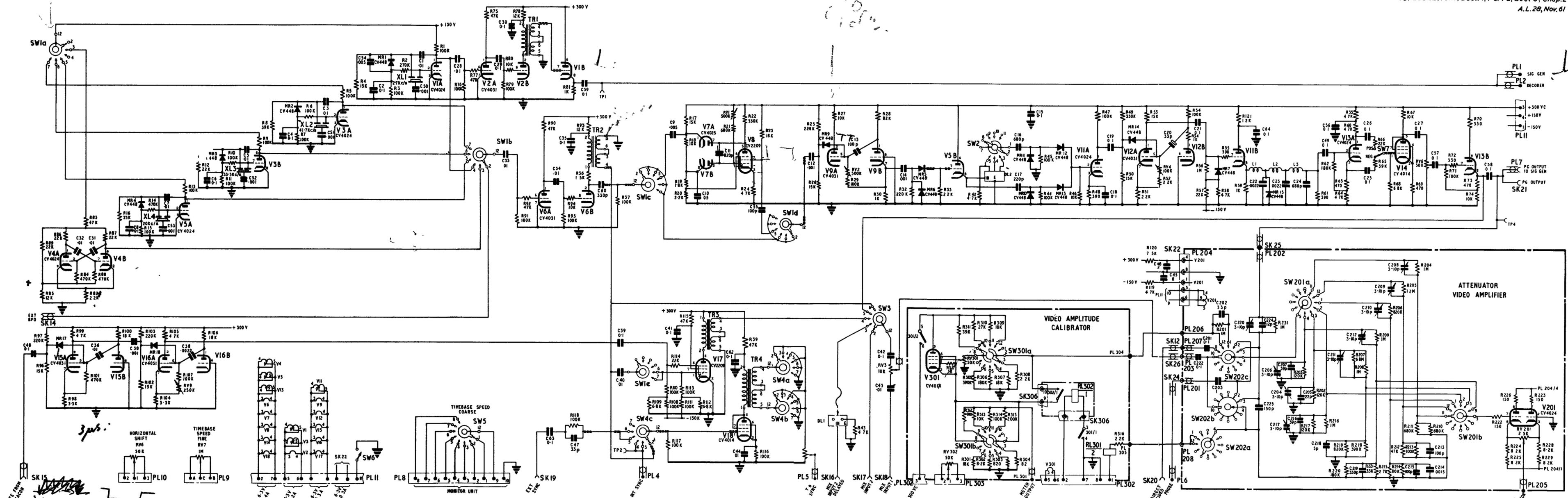


SRI.18118, FGRI.181191 - thermistor unit (U)

R E S T R I C T E D

Fig 60

(A.L.19, Jan.59)



S.R.I. 18118, F.G.R.I. 18119 pulse generator (MA) circuit

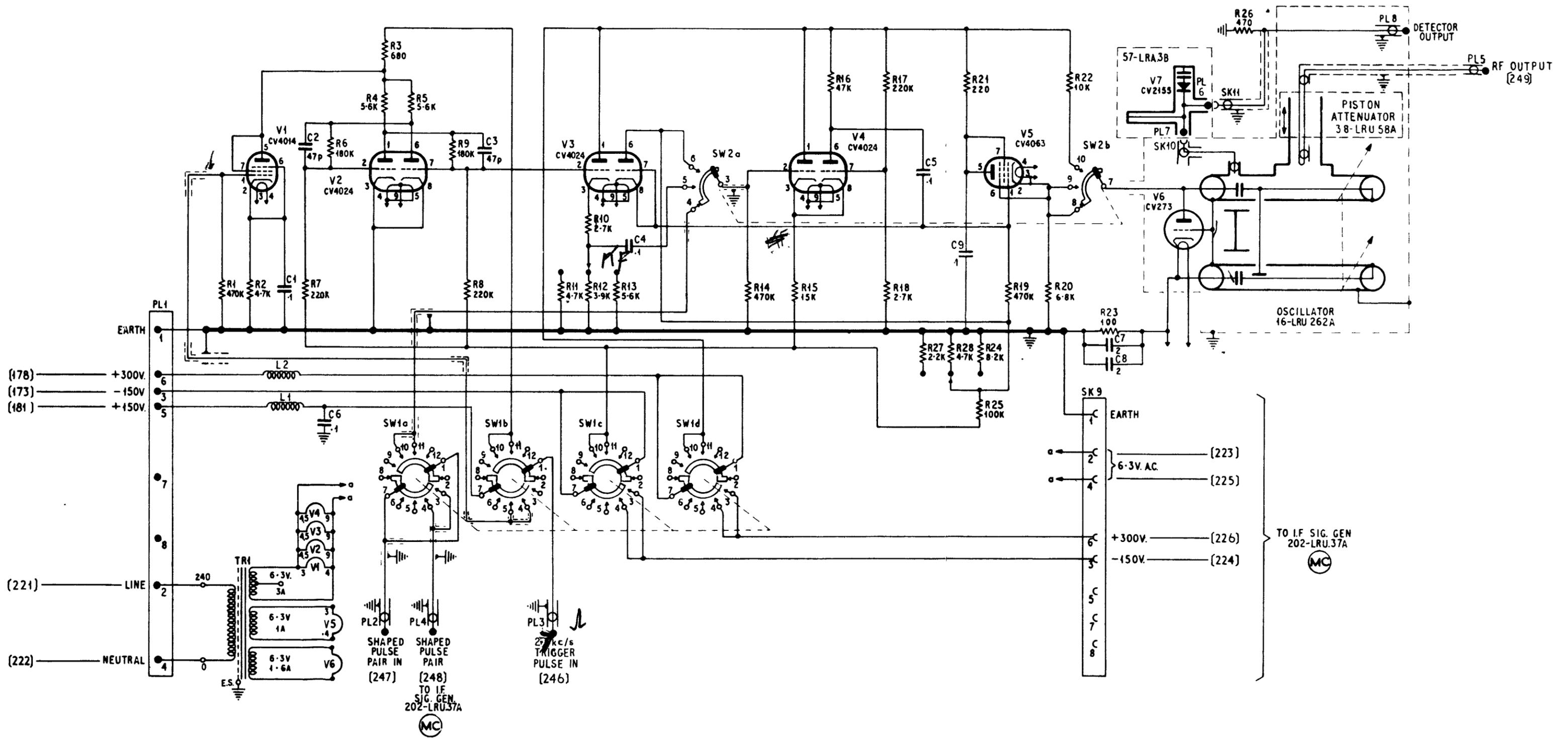
Fig. 61

AIR DIAGRAM
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TP3 or
TP8 Marker Gate GEN.
XJ 75 ST.

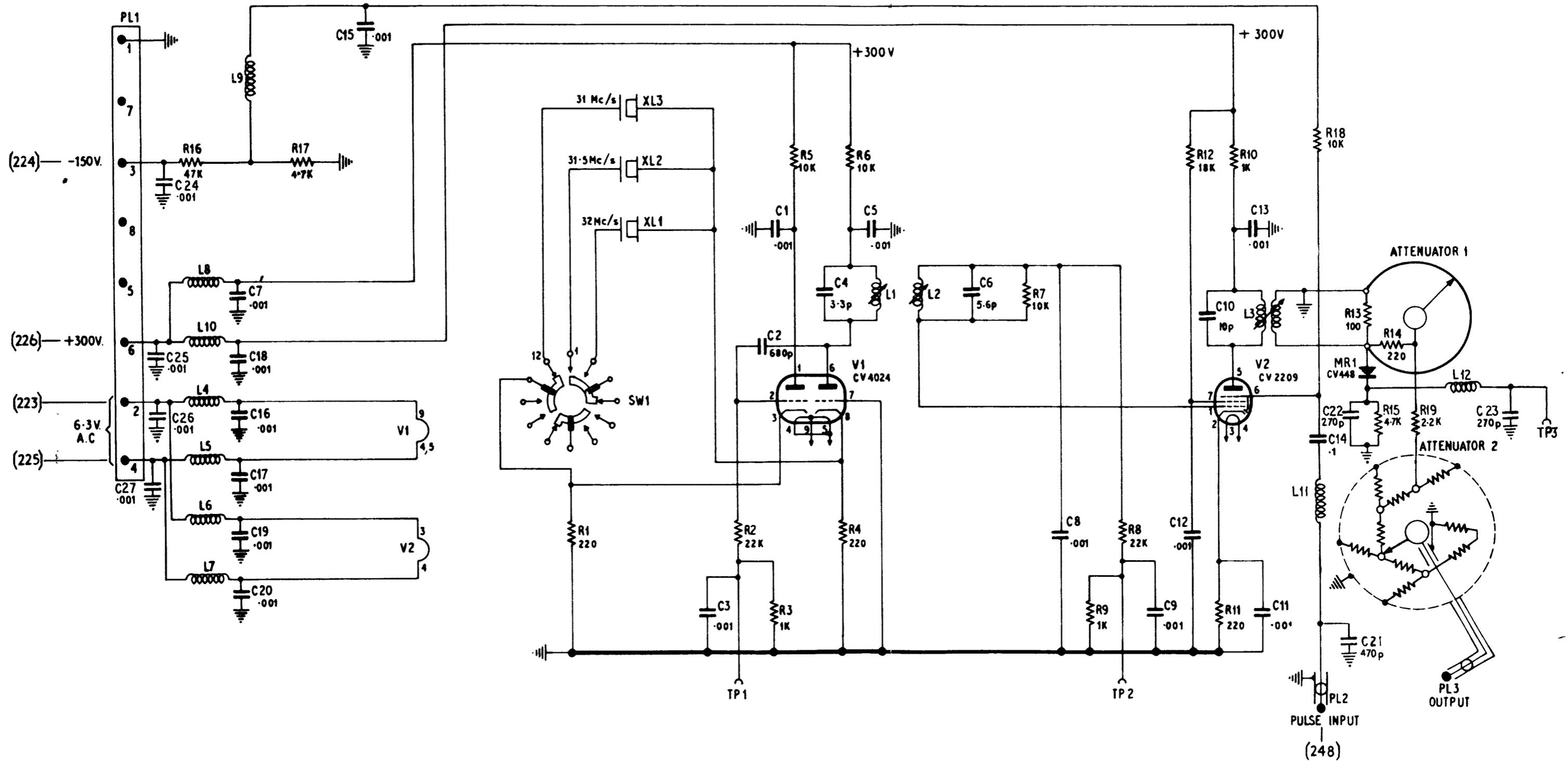
RESISTORS	R1	R2	R7	R6	R3	R4	R5	R9	R8	R11	R10	R12	R13	R14	R15	R16	R17	R18	R27	R28	R21	R24	R25	R19	R20	R22	R23	R26	
CAPACITORS			C1	C2	C6				C3			C4							C5	C9					C7	C8			
MISCELLANEOUS	PL1	TR1	V1	L2	L1	PL2	V2	SW1a	PL4	SW1b	PL3	V3	SW1c	SW2a	SW1d	V4			V5	SW2b	SK9	V7	PL7	SK10	V6	PL6	SK11	PL5	PL8



AIR DIAGRAM
6264W/MIN.
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SRI. 18118, FGRI. 18119 - RF signal generator (MB) - circuit

RESISTORS	R16	R17		R1	R2	R3	R5	R4	R6		R7	R9	R8		R12	R11	R10		R18	R13	R14	R15	R19														
CAPACITORS	C24	C25	C26	C27	C7	C18	C16	C17	C19	C20	C15				C3	C2	C1	C4	C5		C6	C8		C9	C12	C10	C13	C11	C14	C21	C22			C23			
MICSELLANEOUS	PL1	L8	L10	L4	L5	L6	L7	L9							SW1	XL1	XL2	XL3		TP1	V1	L1	L2			TP2		V2	L3	PL2	L11	MR1	ATT.1	ATT.2	PL3	L12	TP3

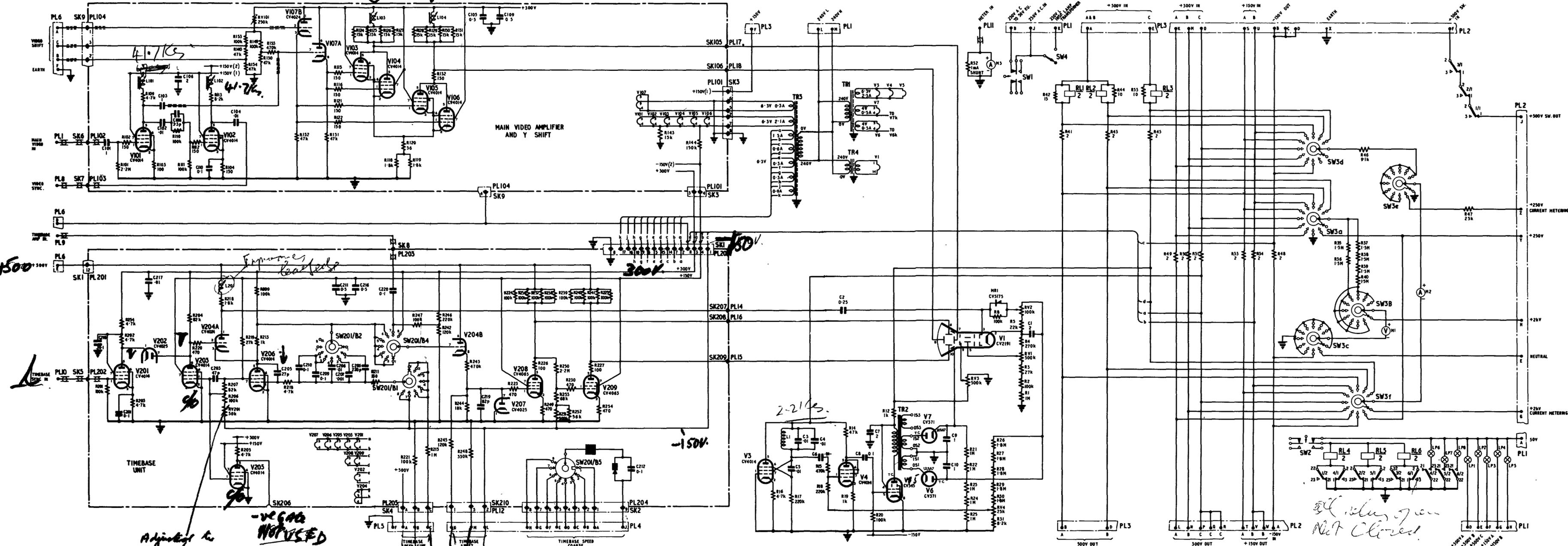


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SRI. 18118, FGRI. 18119 — IF signal generator (MC) - circuit

Fig 63

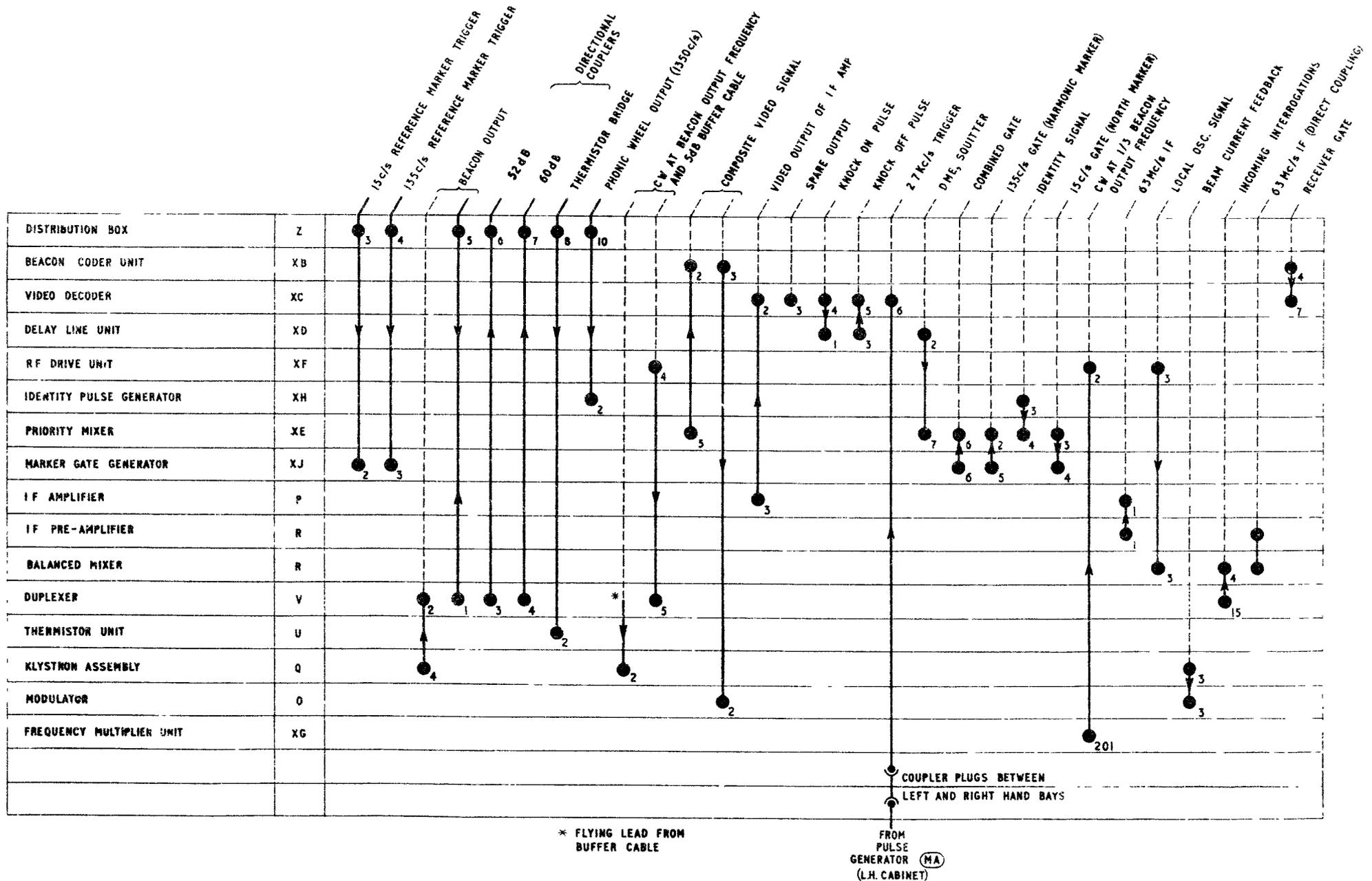
41.7



S.R.I. 18118 F.G.R.I. 18119 Monitoring unit (L) - circuit

AIR DIAGRAM
6264T/MIN.

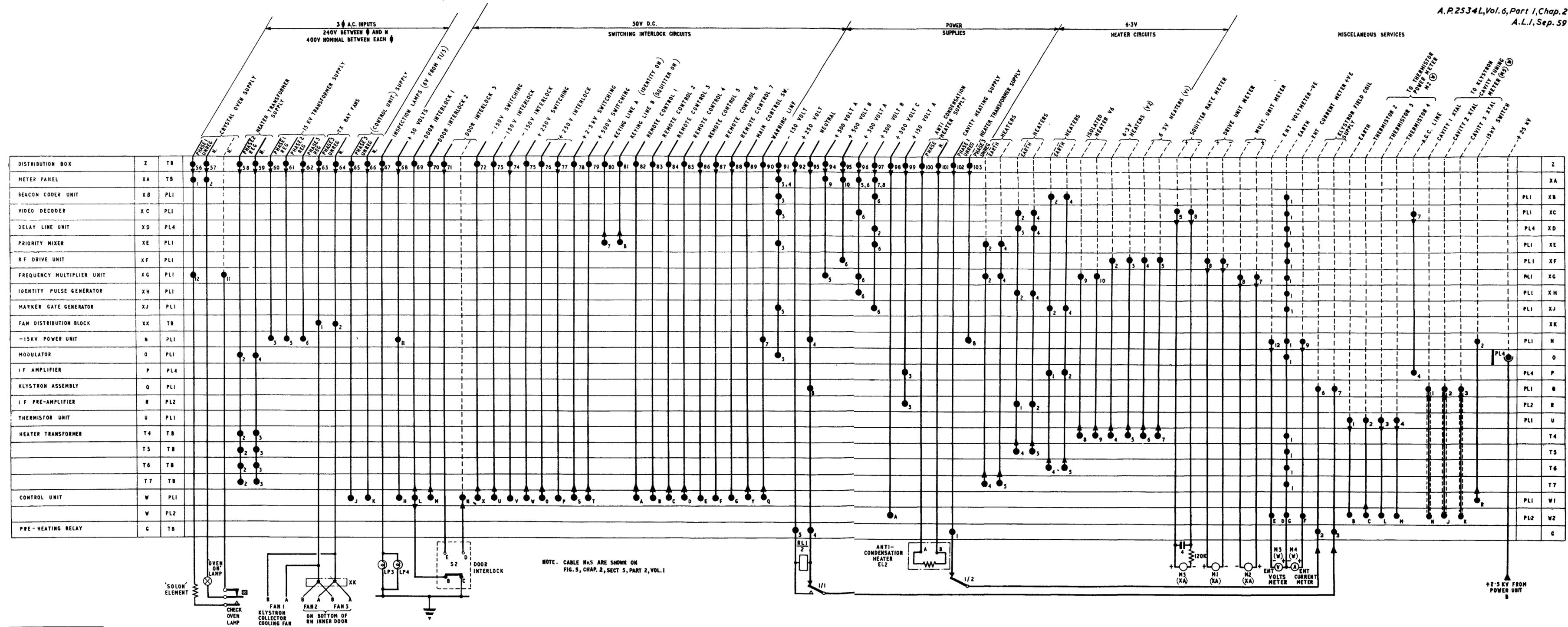
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AIR DIAGRAM
6264AN/MIN.

SRI.18118 FGRI.18119 : coaxial interconnections

Fig.66

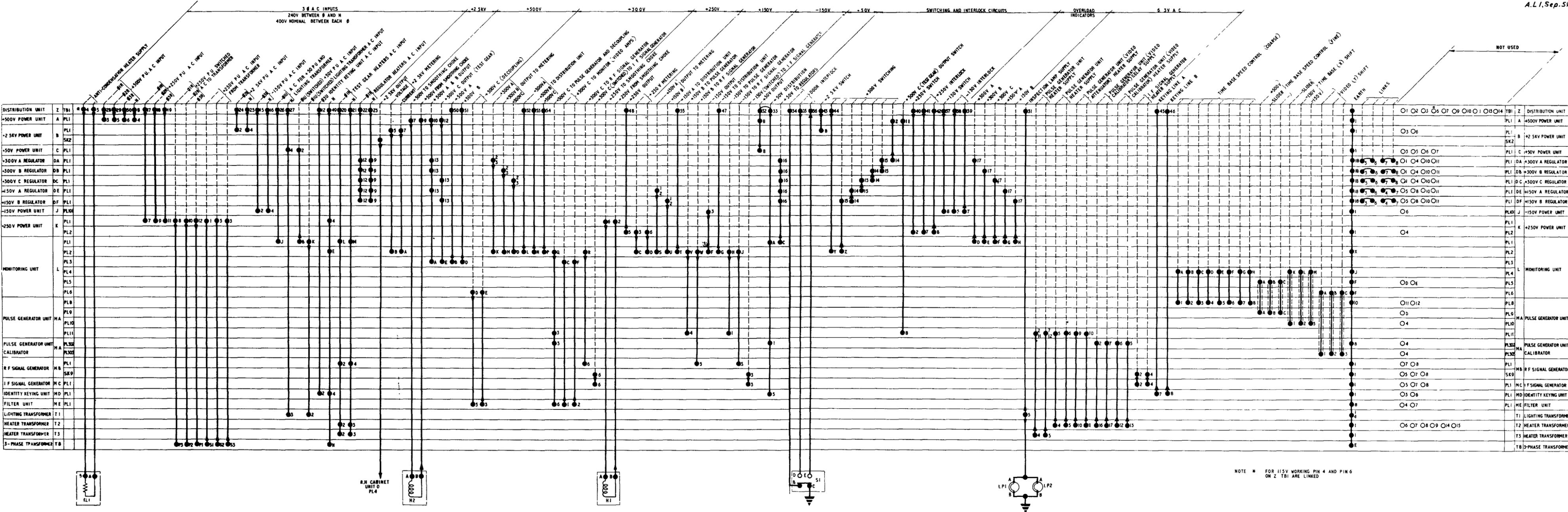


NOTE. CABLE N°S ARE SHOWN ON FIG. 5, CHAP. 2, SECT 5, PART 2, VOL. 1

AIR DIAGRAM
6264AS/MIN.
ISSUE 1 PREPARED BY MINISTRY OF SUPPLY FOR PROMULGATION BY AIR MINISTRY

SRI. I8118 FGRI. I8119-R.H. cabinet: interconnections

Fig 67



AIR DIAGRAM
6264AT/MIN.
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FOR PROMOTION BY AIR FORCE

SRI.18118 FGRI.18119-L.H. cabinet : interconnections

Fig. 68